

Q WIRE:



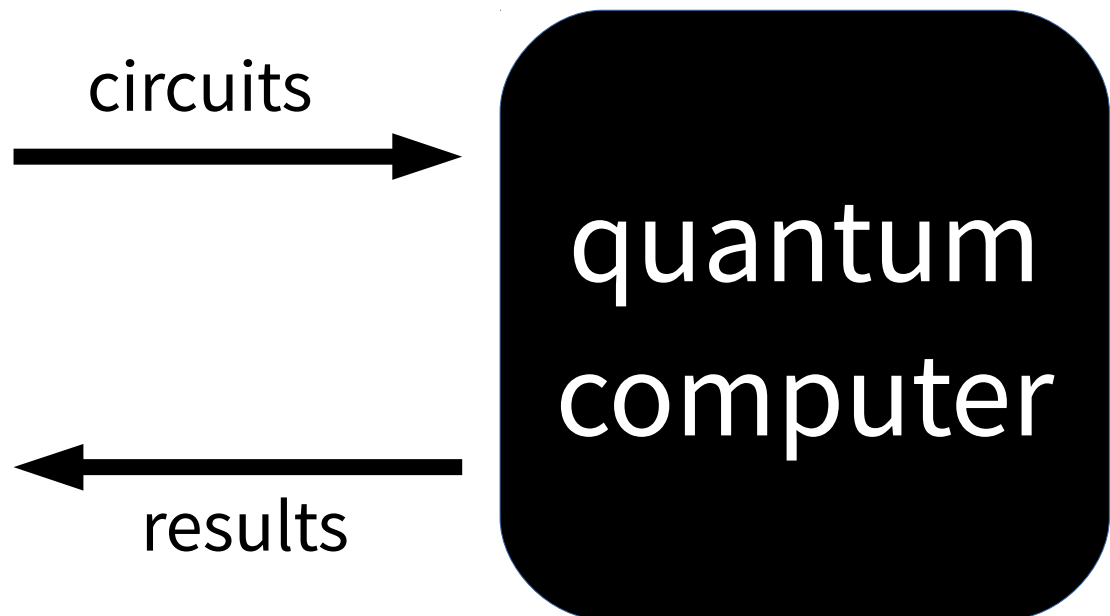
A core language for quantum circuits

Jennifer Paykin, Robert Rand, Steve Zdancewic
University of Pennsylvania

POPL 2017, Paris, France



The Circuit Model



Building Blocks of Quantum Computing



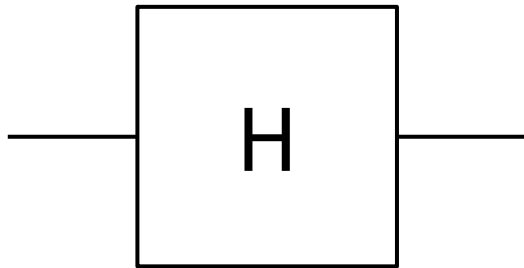
Building Blocks of Quantum Computing

qubits $|0\rangle$ or $|1\rangle$ or $\frac{1}{\sqrt{2}}|0\rangle + \frac{1}{\sqrt{2}}|1\rangle$

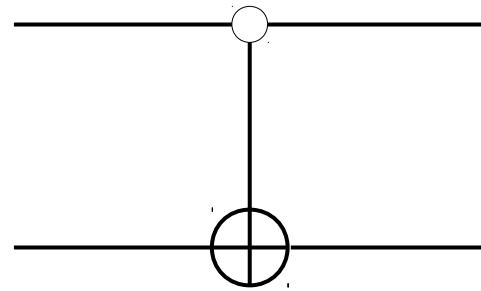


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Hadamard

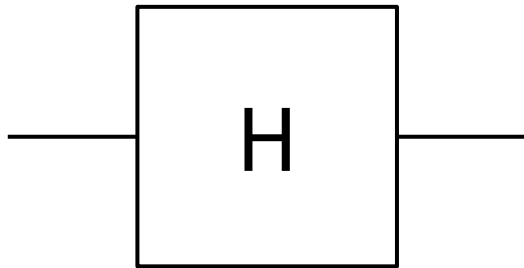


controlled not (CNOT)

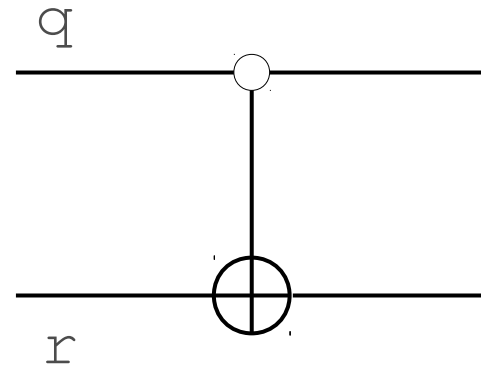


Building Blocks of Quantum Computing

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Hadamard



controlled not (CNOT)

“if q then not r”



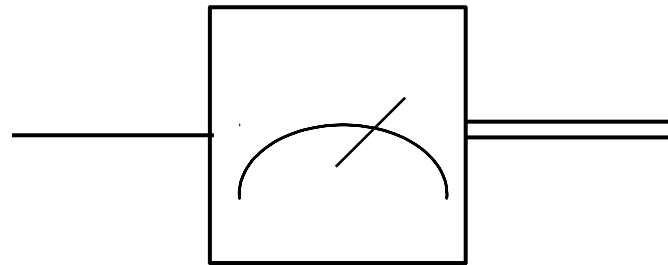
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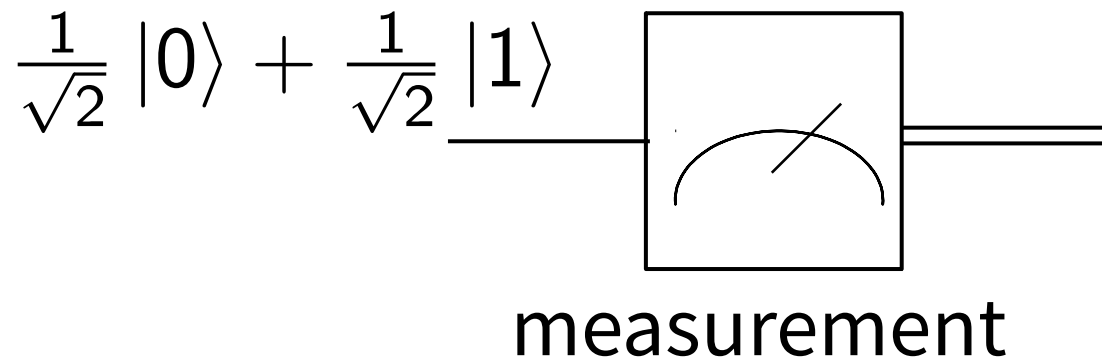


measurement



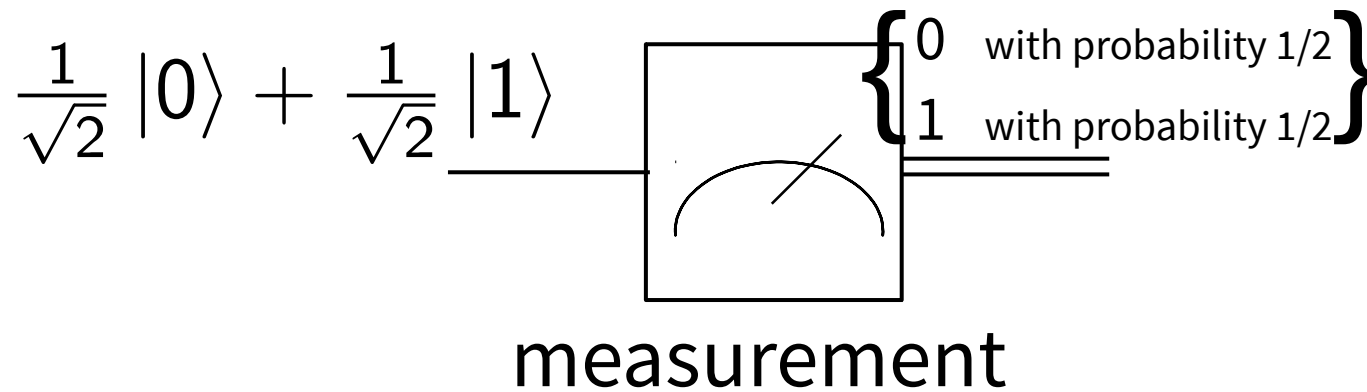
Building Blocks of Quantum Computing

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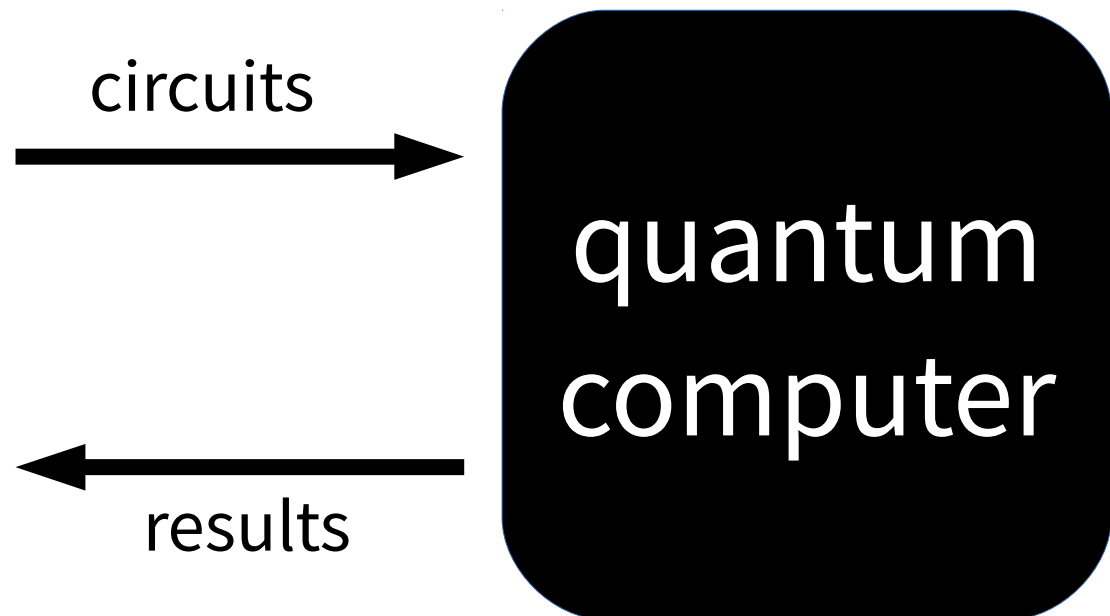
Building Blocks of Quantum Computing

qubits $|0\rangle$ or $|1\rangle$ or $\frac{1}{\sqrt{2}}|0\rangle + \frac{1}{\sqrt{2}}|1\rangle$



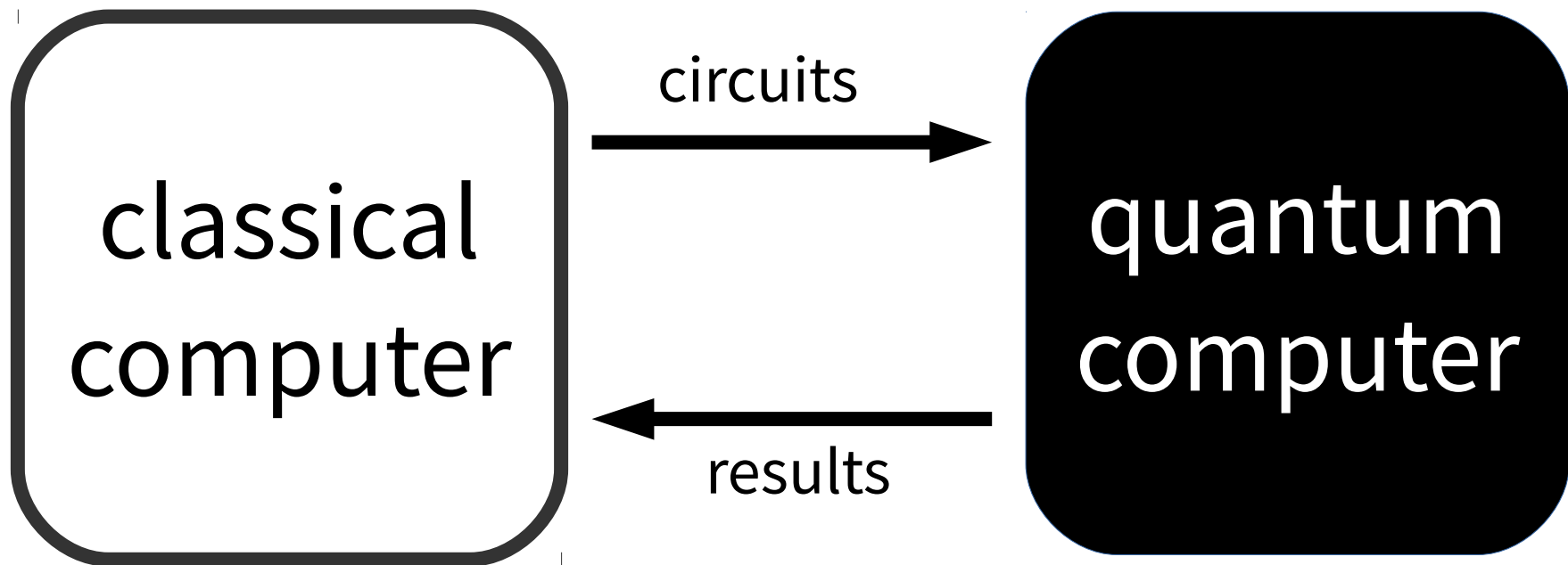
The QRAM Model of Quantum Computing

Knill, 1996

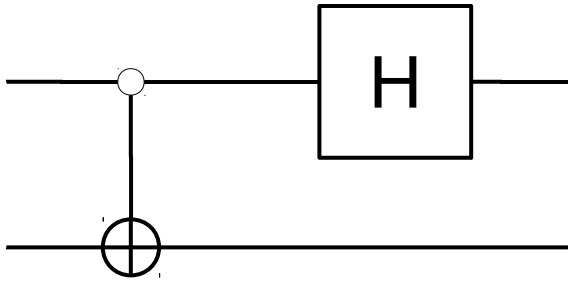


The QRAM Model of Quantum Computing

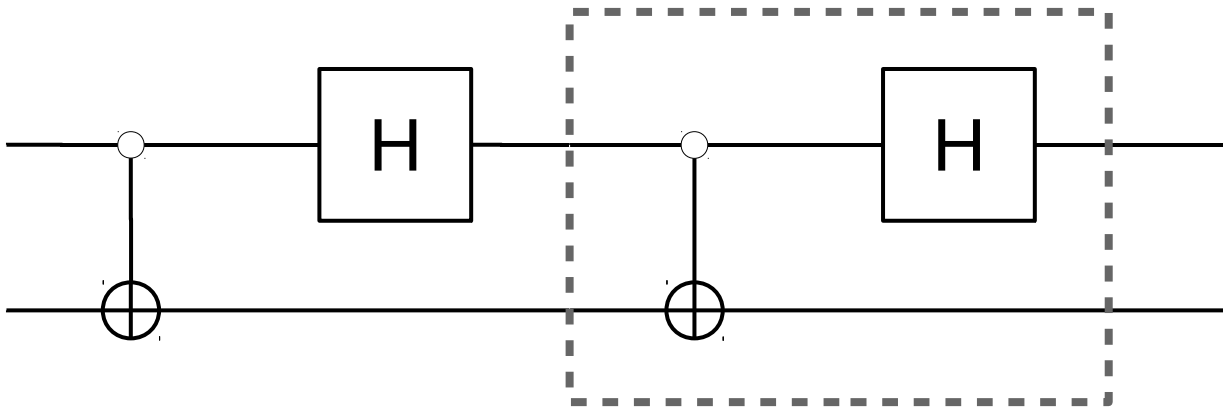
Knill, 1996



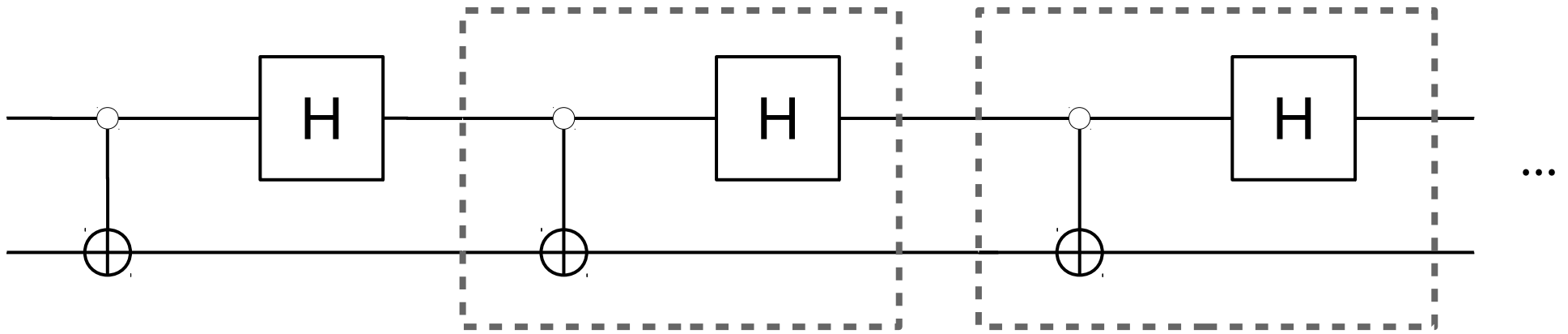
n-ary Composition



n-ary Composition

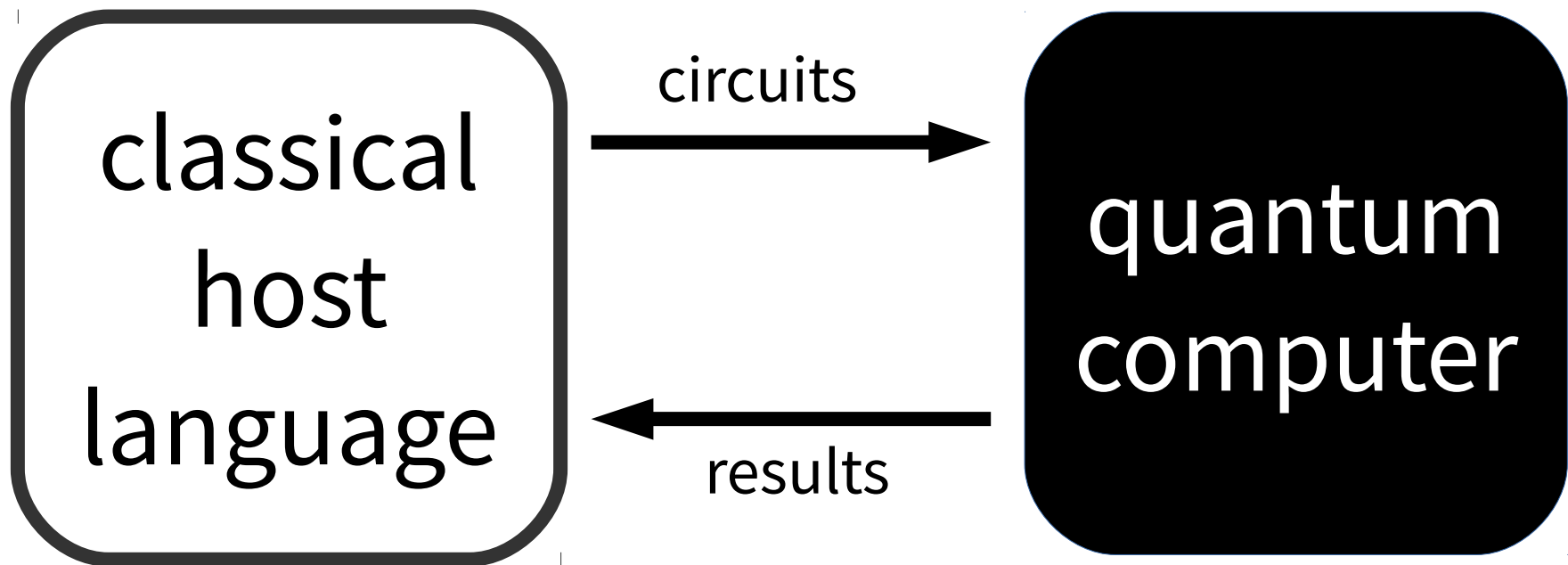


n-ary Composition



The QRAM Model of Quantum Computing

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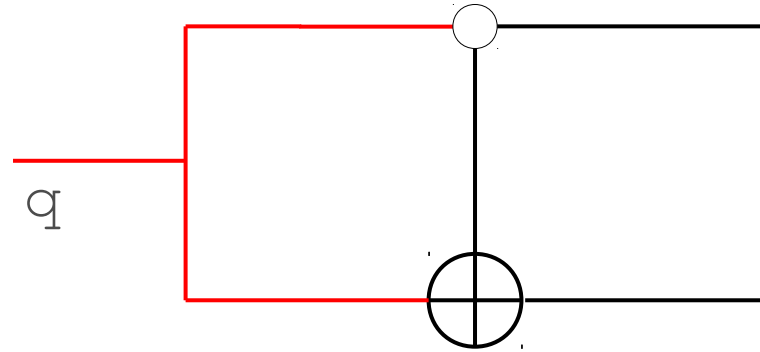


Quipper (Green et al, 2013)

LIQ*Ui* | \rangle (Wecker and Svore, 2014)



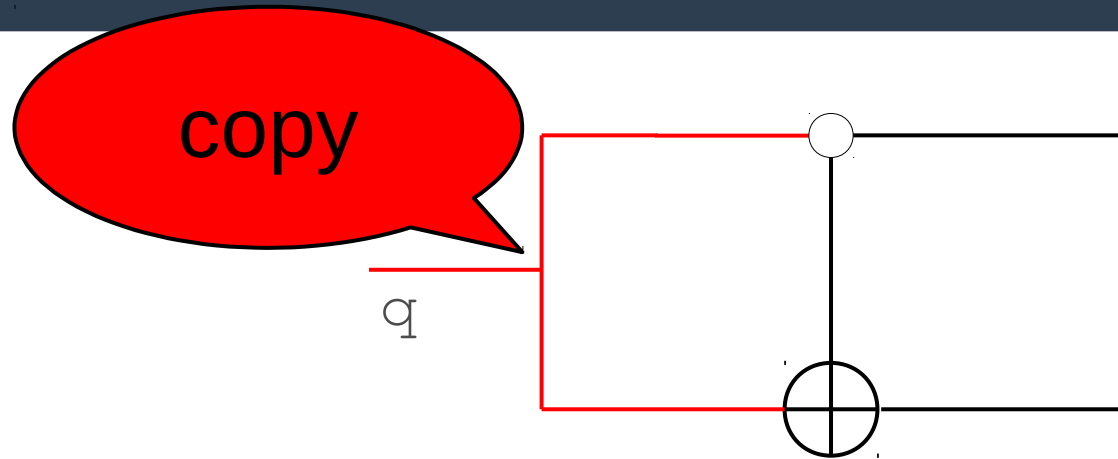
Problem: Ill-Formed Circuits



“q if q then not q”



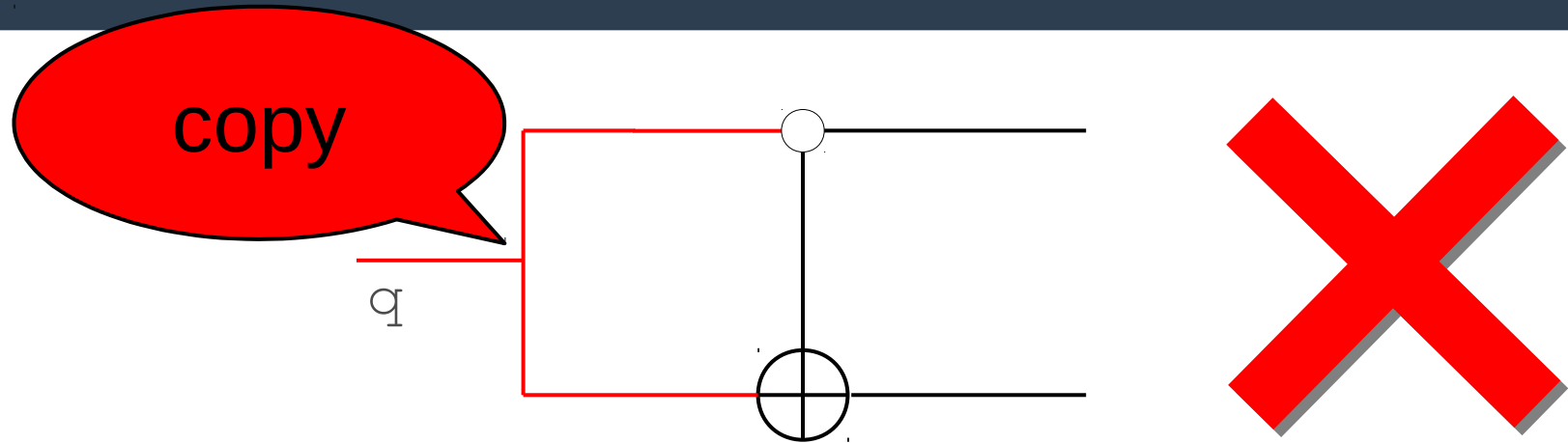
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Problem: Ill-Formed Circuits



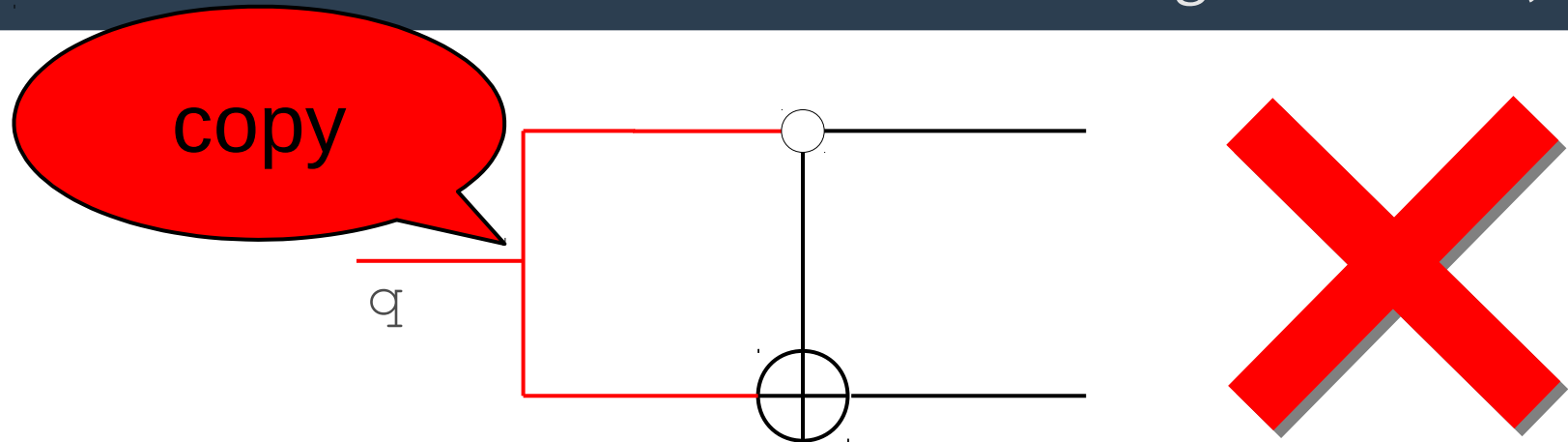
“q if q then not q”

no-cloning theorem: a qubit cannot be copied



Problem: Ill-Formed Circuits

Selinger & Valiron, 2009



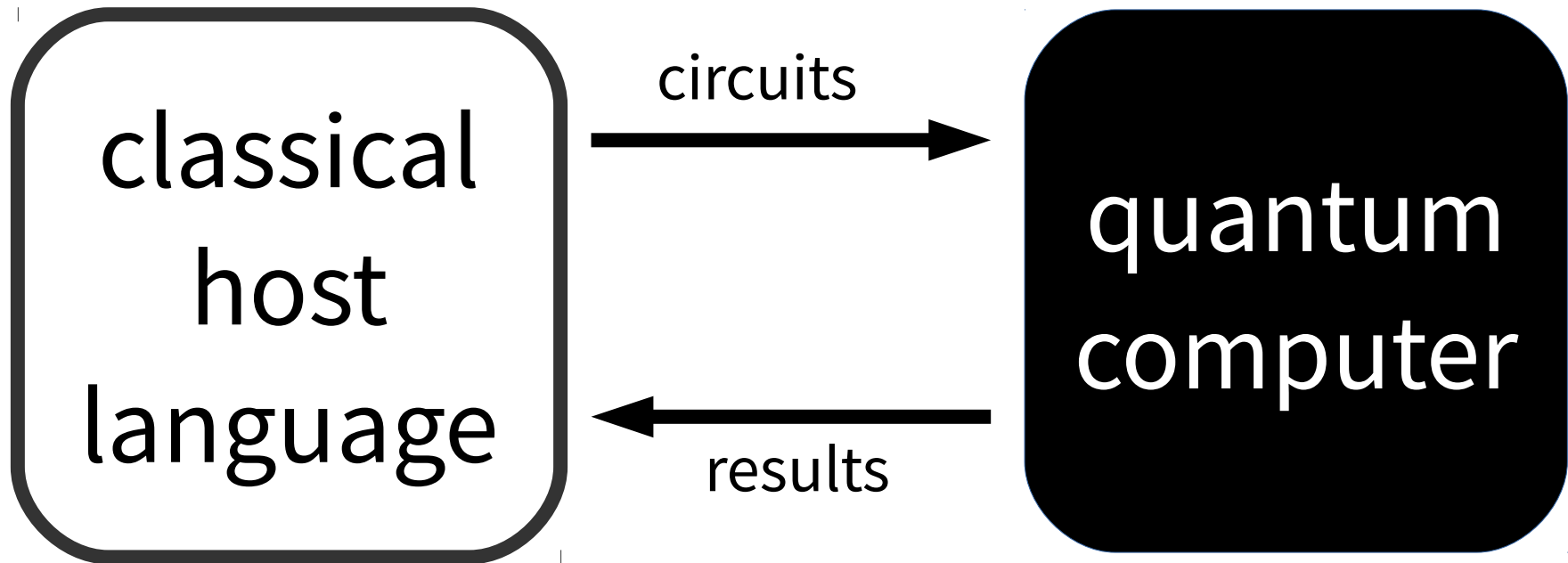
“q if q then not q”

no-cloning theorem: a qubit cannot be copied

linear types \Rightarrow no cloning



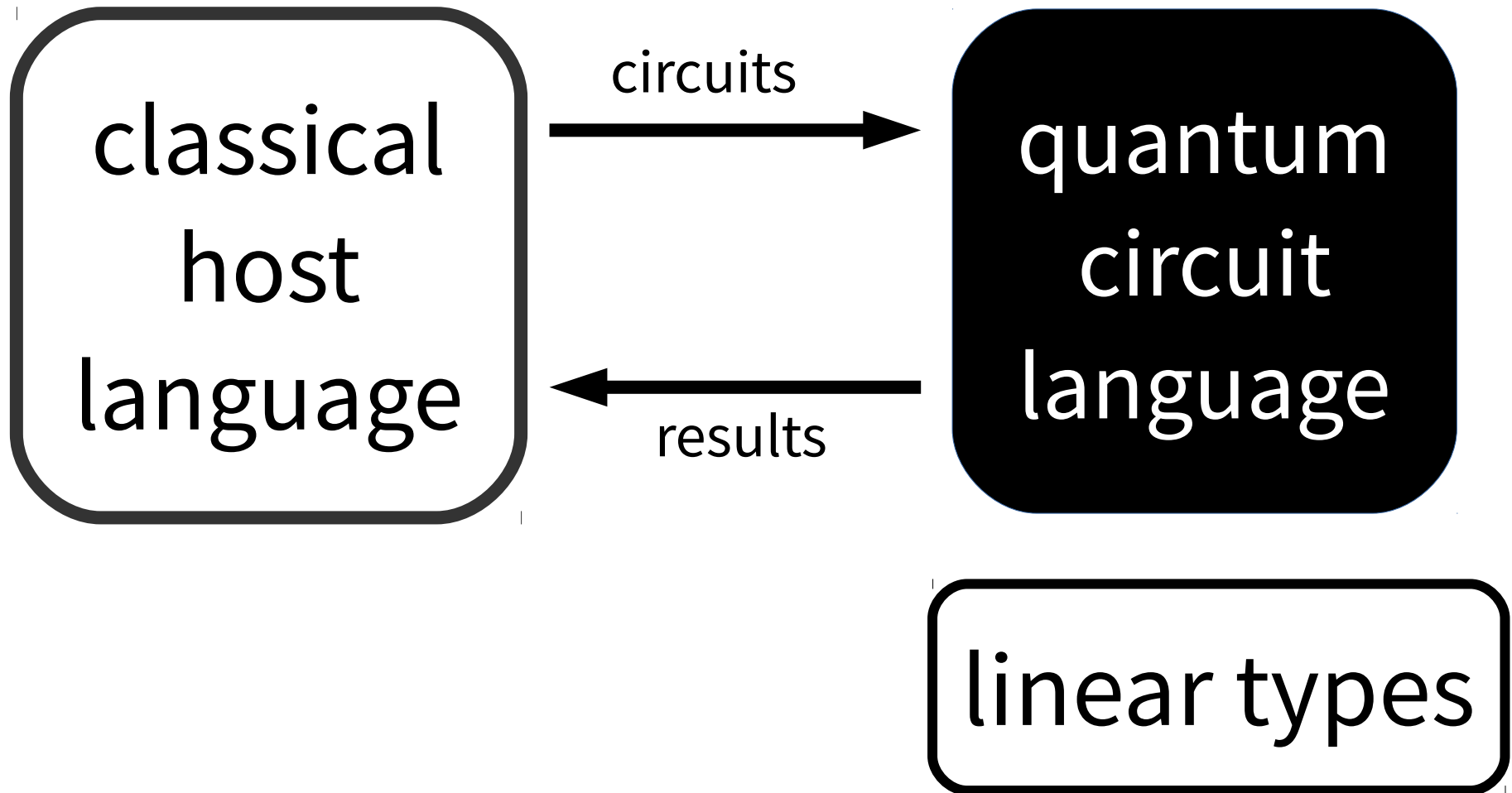
A linear QRAM model?



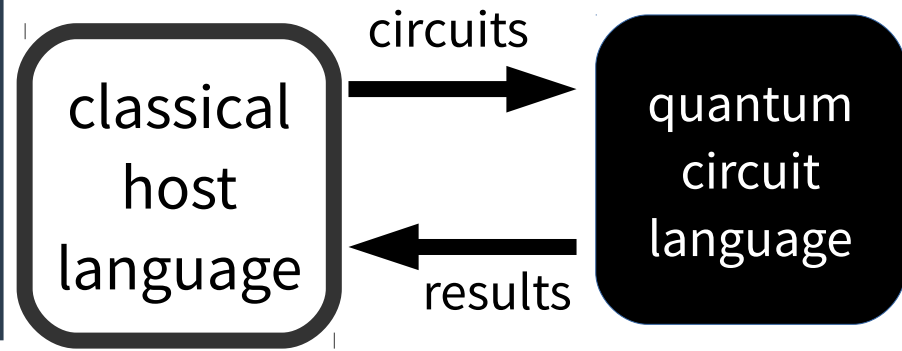
linear types?



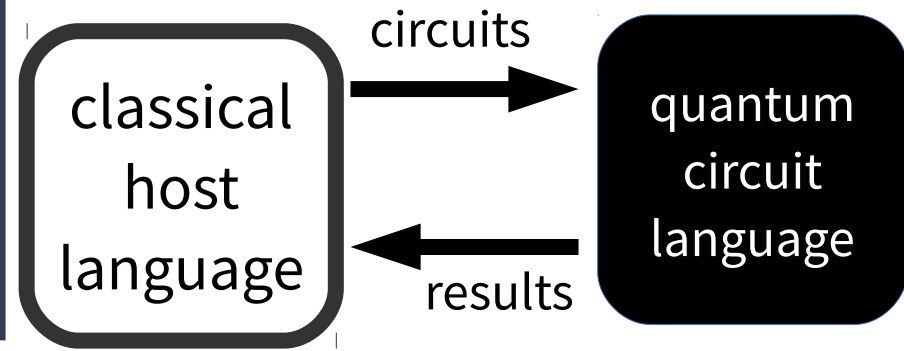
Introducing QWIRE...



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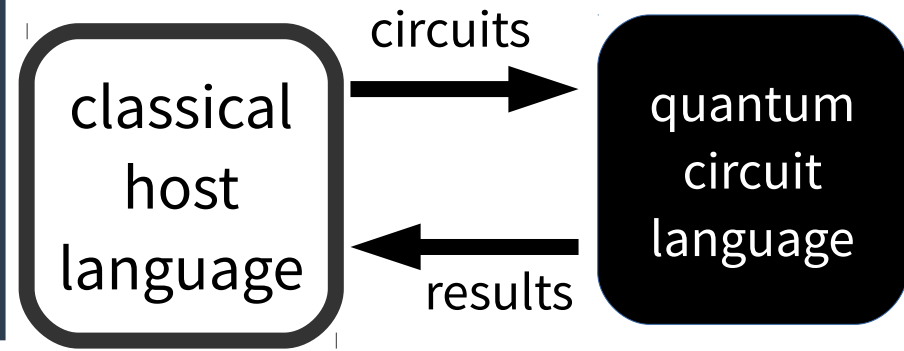
Introducing QWIRE...



- A core language for quantum circuits



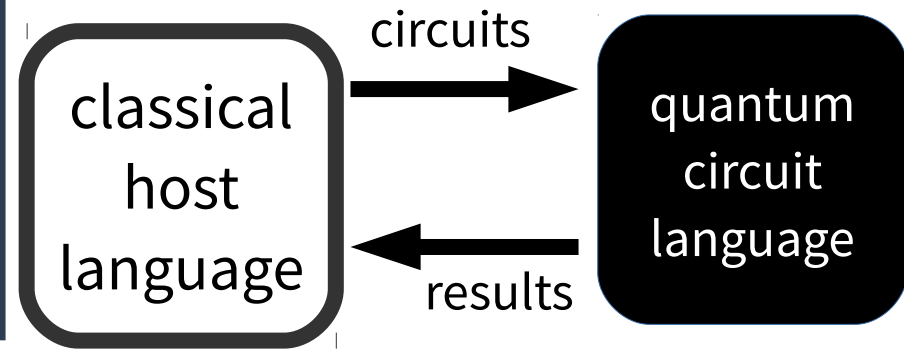
Introducing QWIRE...



- A core language for quantum circuits
- Safe
 - linear types for wires
 - type safety & strong normalization
 - denotational semantics



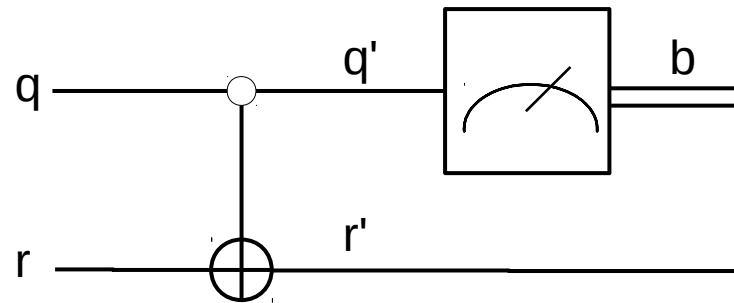
Introducing QWIRE...



- **A core language for quantum circuits**
- **Safe**
 - linear types for wires
 - type safety & strong normalization
 - denotational semantics
- **Expressive**
 - structure based on the QRAM model
 - embedded in your favorite classical host language



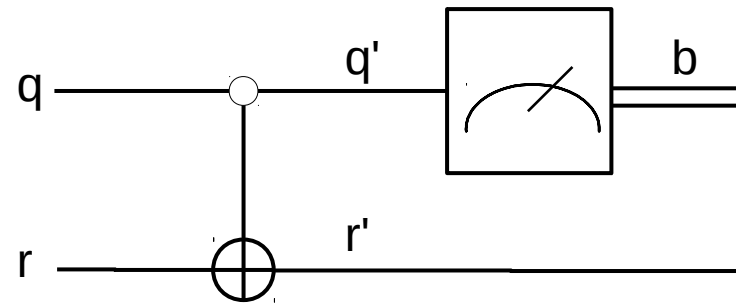
Quantum Circuits



(q', r') \leftarrow gate CNOT (q, r) ;
b \leftarrow gate meas q' ;
output (b, r')



Quantum Circuits



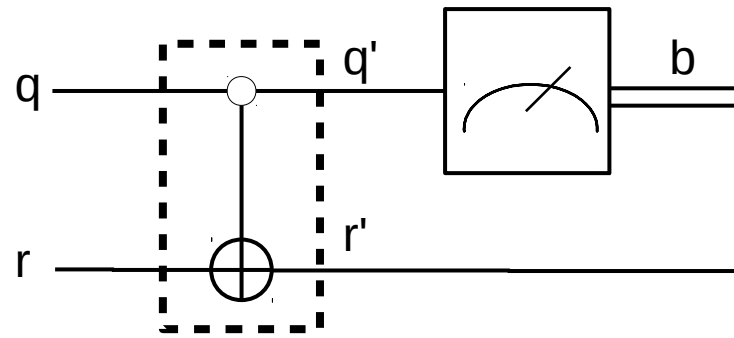
wire names



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Quantum Circuits



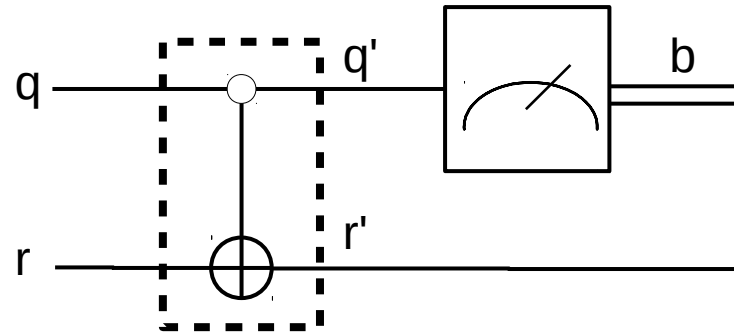
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(q', r') \leftarrow gate [CNOT] (q, r) ;
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Quantum Circuits

let binding



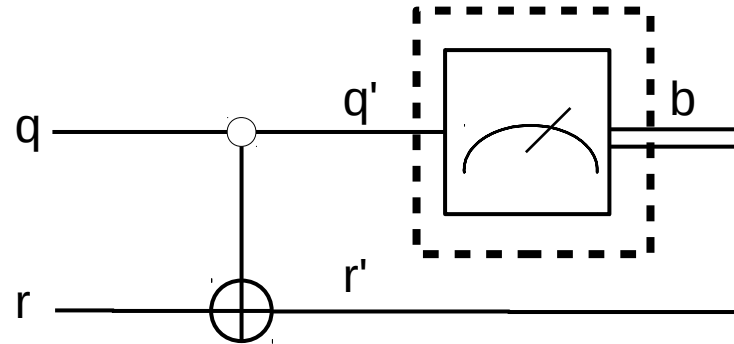
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Quantum Circuits

let binding



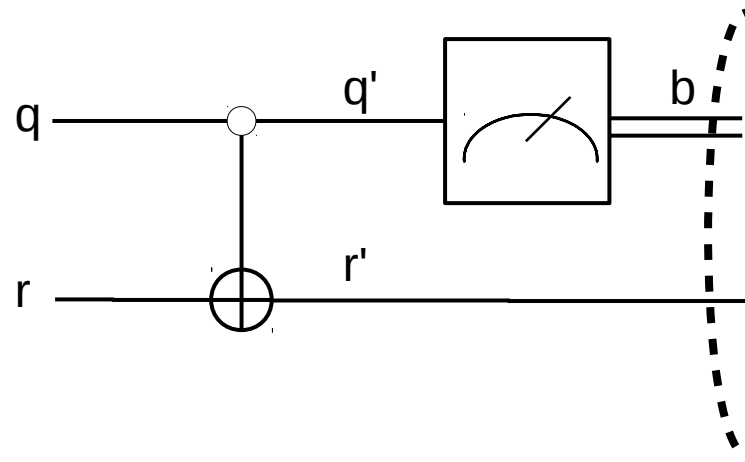
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Quantum Circuits

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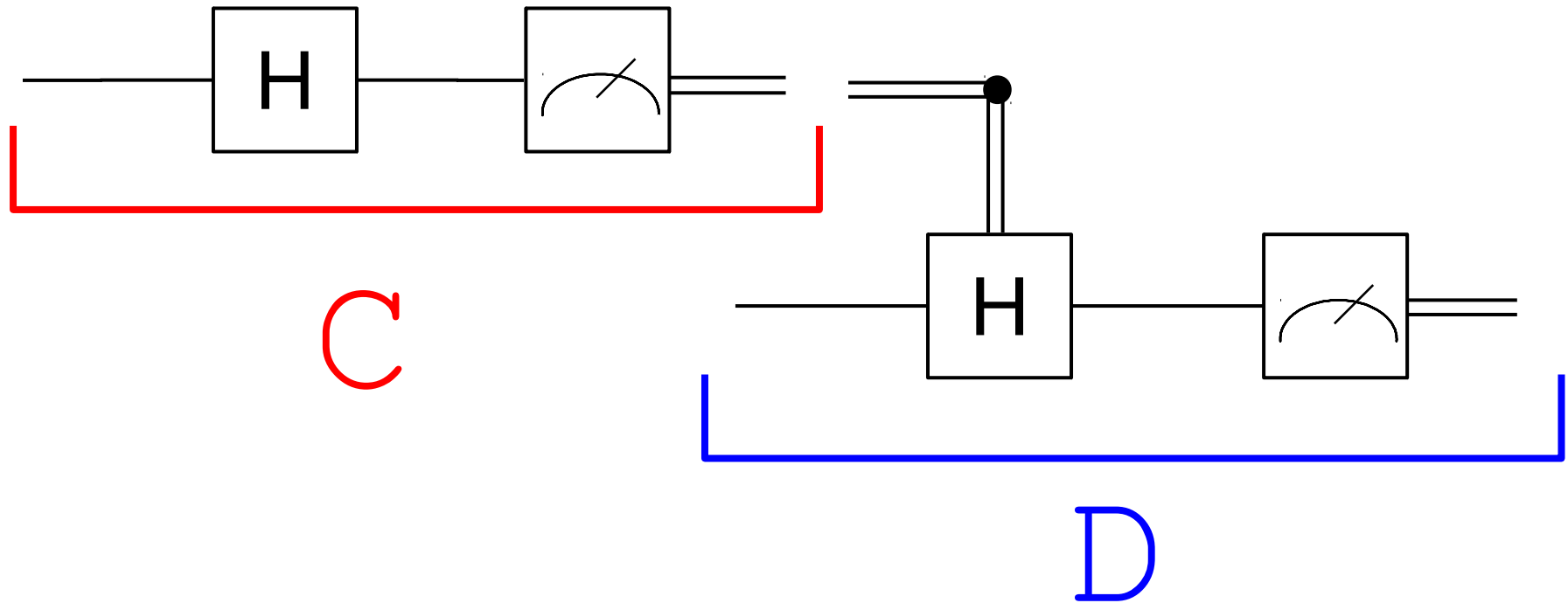


wire names

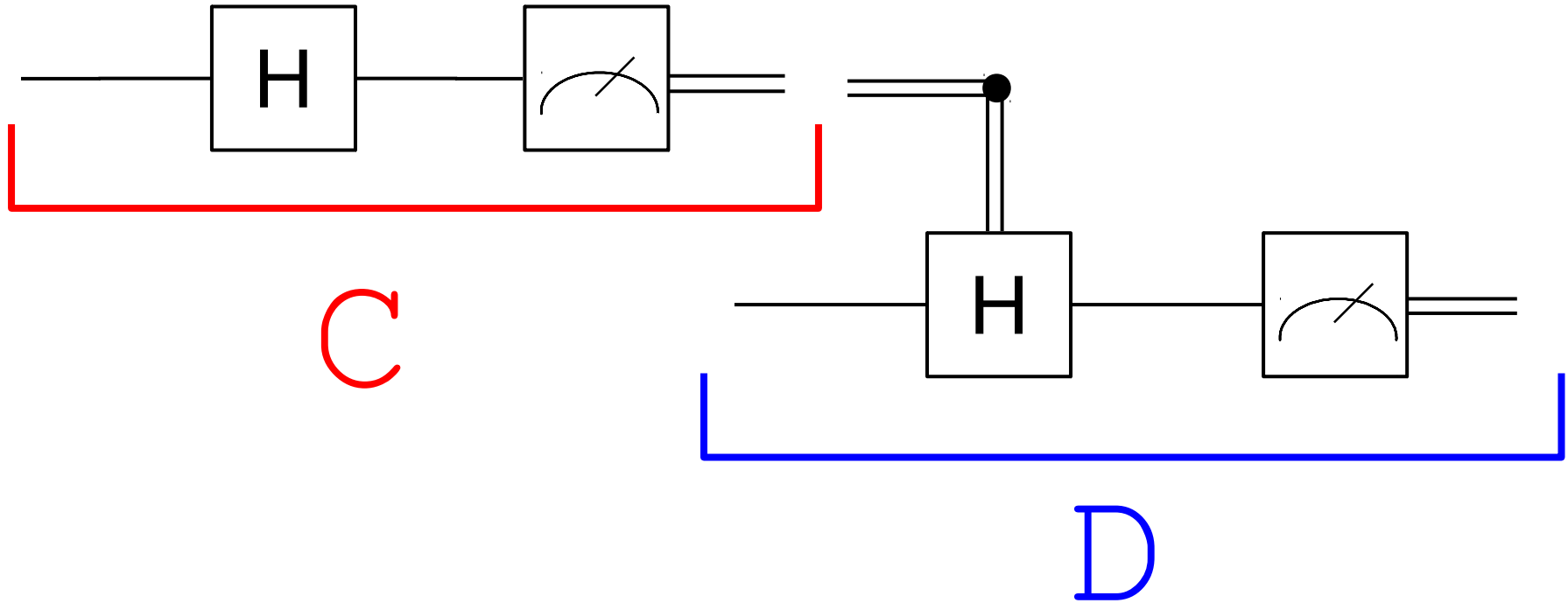
```
(q', r') ← gate CNOT (q, r);  
b       ← gate meas q';  
output (b, r');
```



Composition



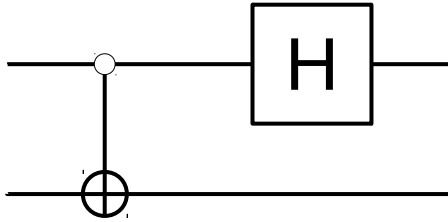
Composition



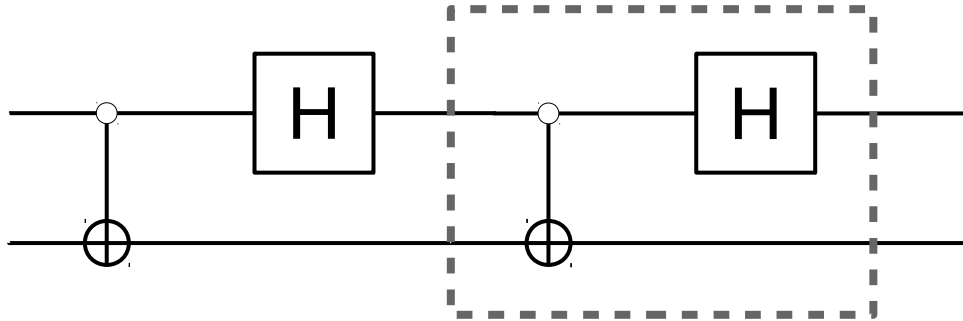
$b \leftarrow C; D$



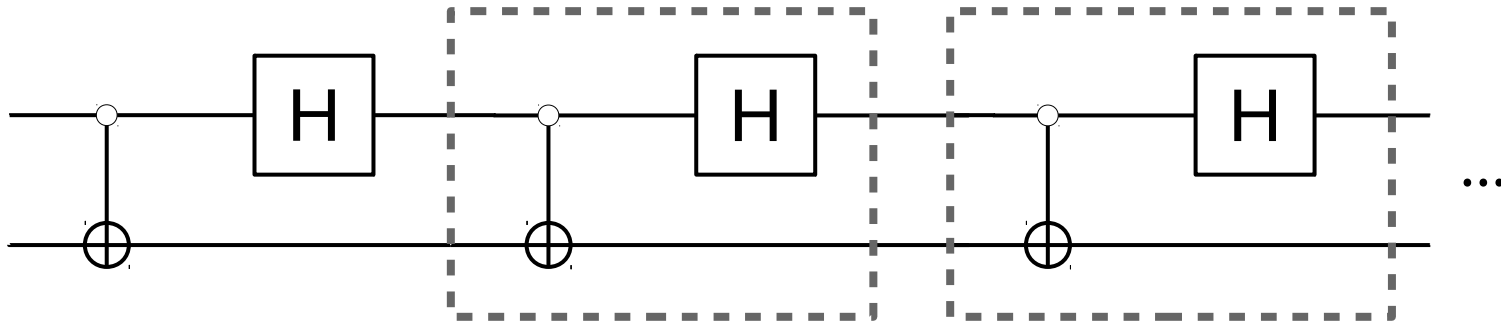
n-ary Composition?



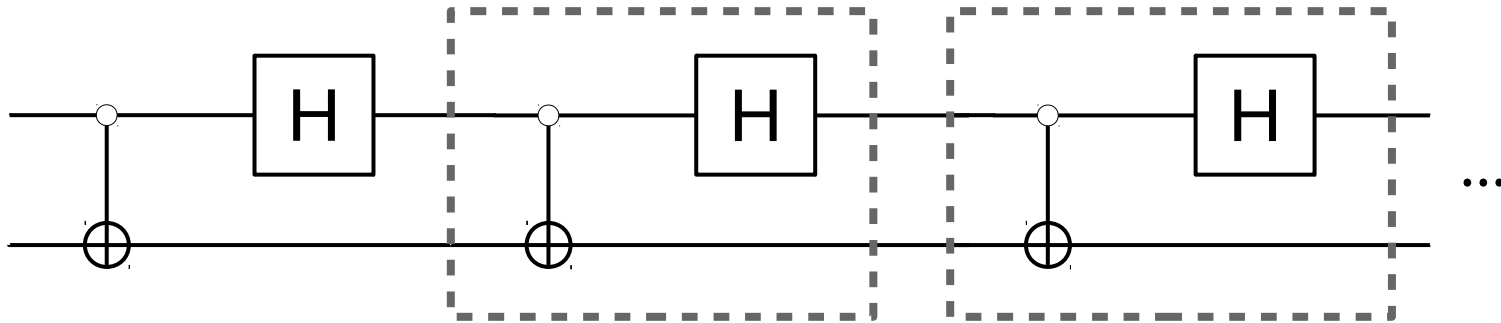
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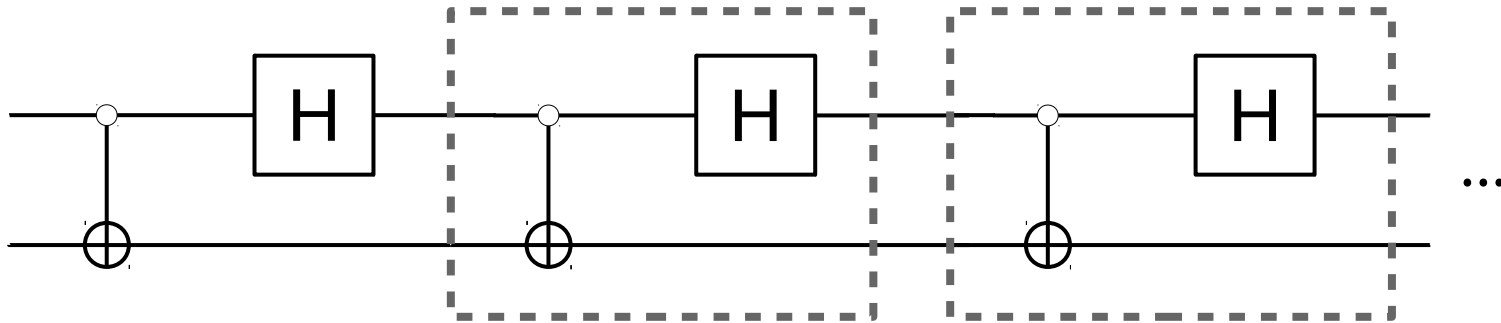
n-ary Composition?



```
(q, r) ← gate CNOT (q, r) ;  
q      ← gate H q ;
```



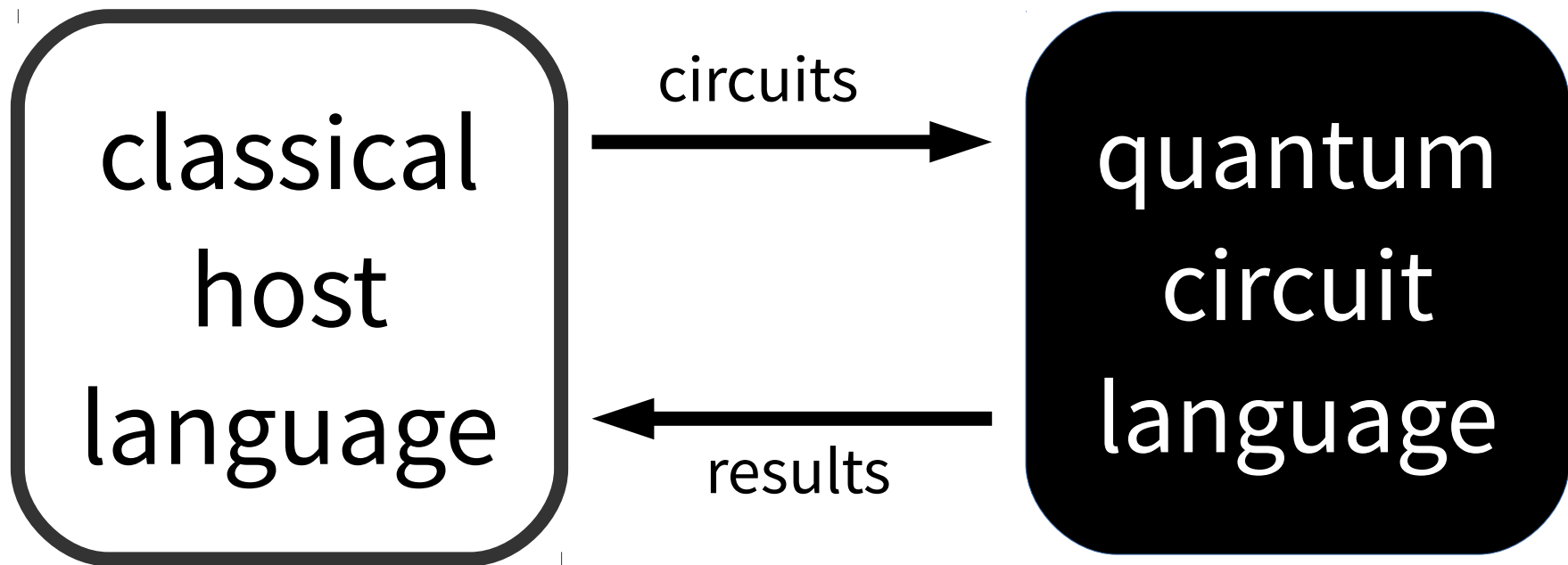
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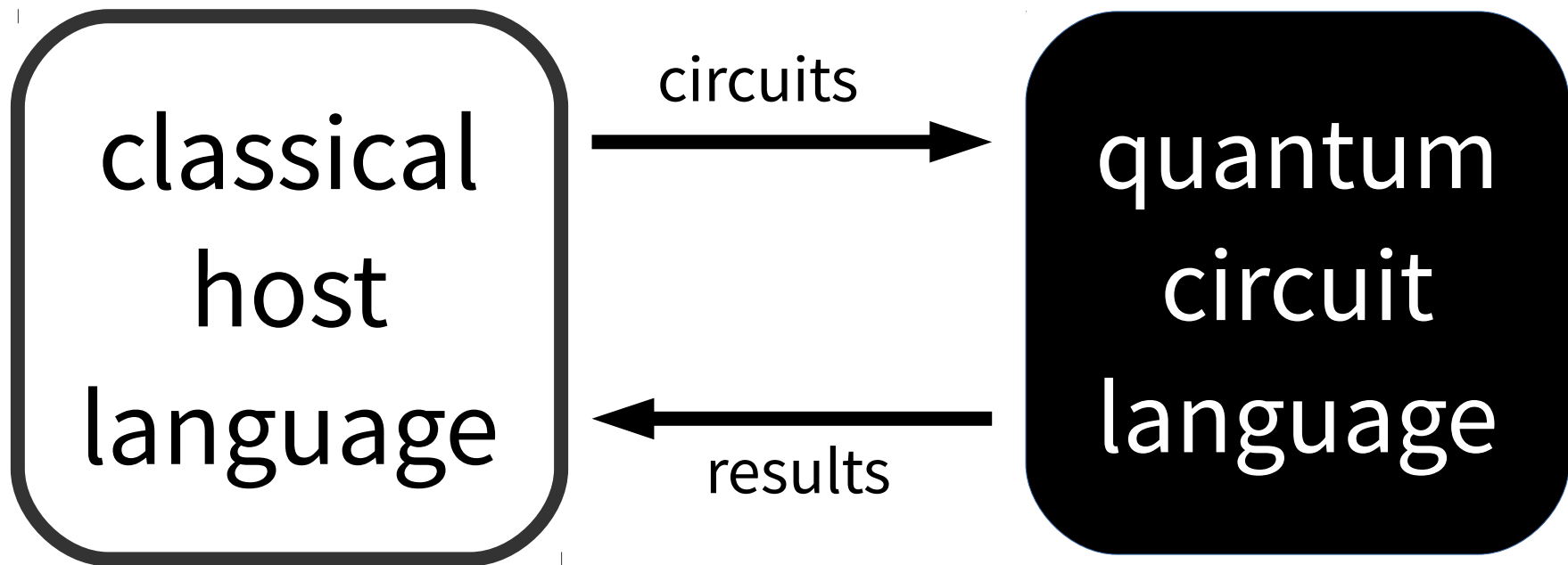
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(q, r) ← gate CNOT (q, r) ;  
q      ← gate H q ;  
(q, r) ← gate CNOT (q, r) ;  
q      ← gate H q ;  
... .
```



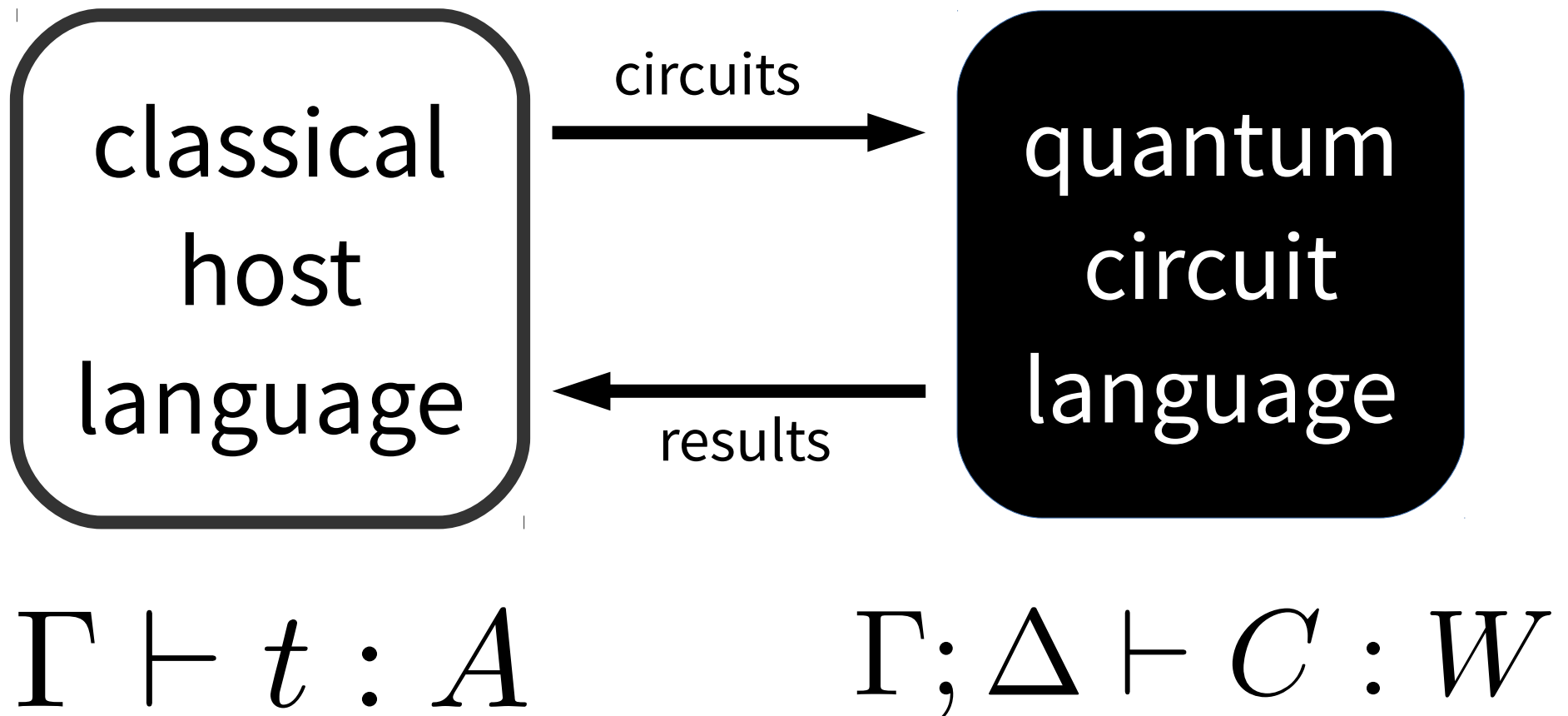
Typing Judgments in QWIRE



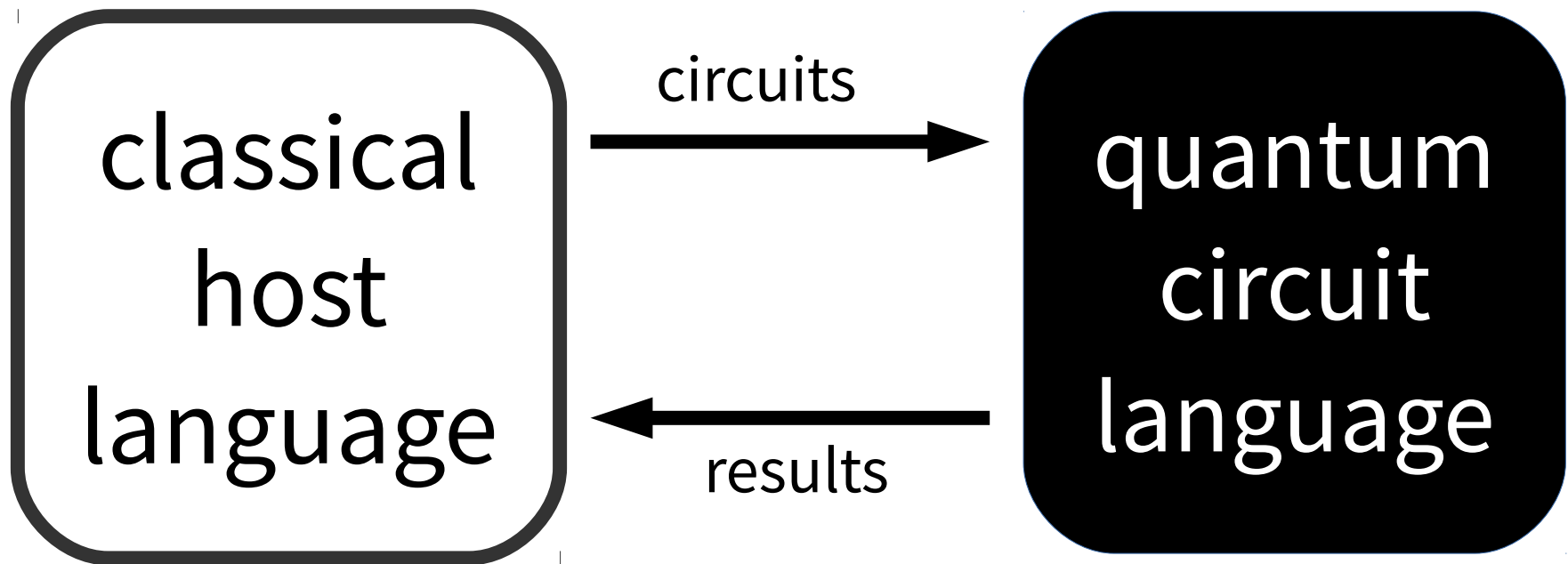
Typing Judgments in Q WIRE


$$\Gamma \vdash t : A$$


Typing Judgments in Q WIRE



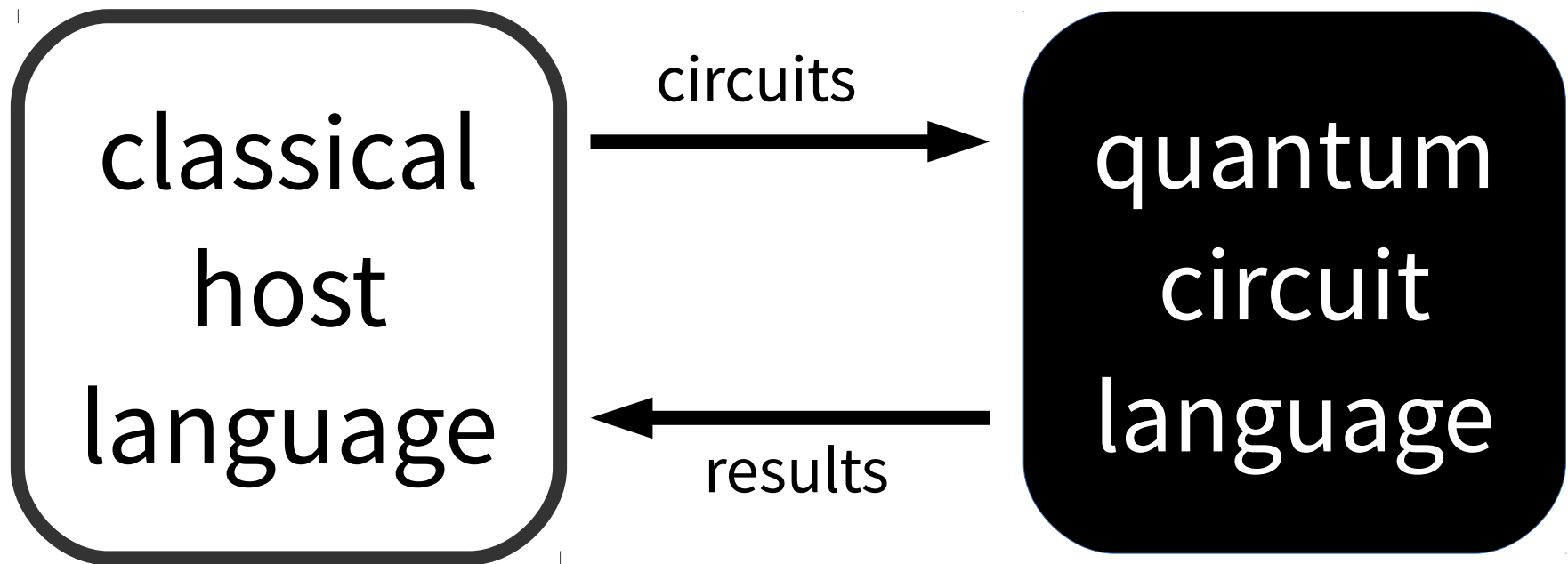
Typing Judgments in Q WIRE


$$\Gamma \vdash t : A$$
$$\Gamma; \underline{\Delta} \vdash C : \underline{W}$$

linear wire types

Typing Judgments in QWIRE

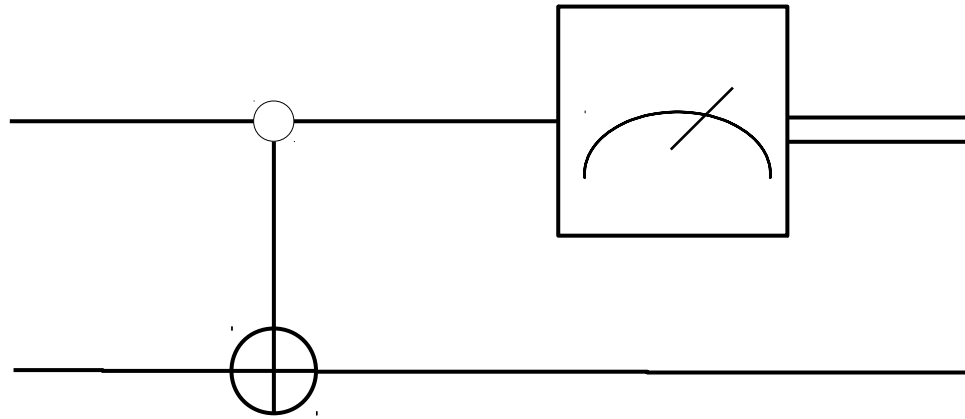
Benton, 1995



$\underline{\Gamma} \vdash t : \underline{A}$
host language types

$\underline{\Gamma}; \underline{\Delta} \vdash C : \underline{W}$
linear wire types

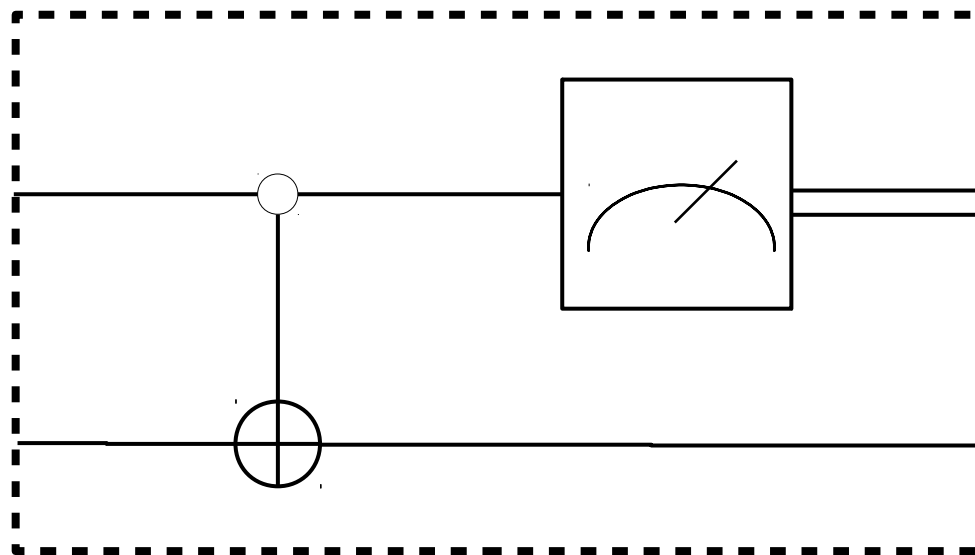
Boxes



$\Gamma; q : \text{qubit}, r : \text{qubit} \vdash C : \text{bit} \otimes \text{qubit}$

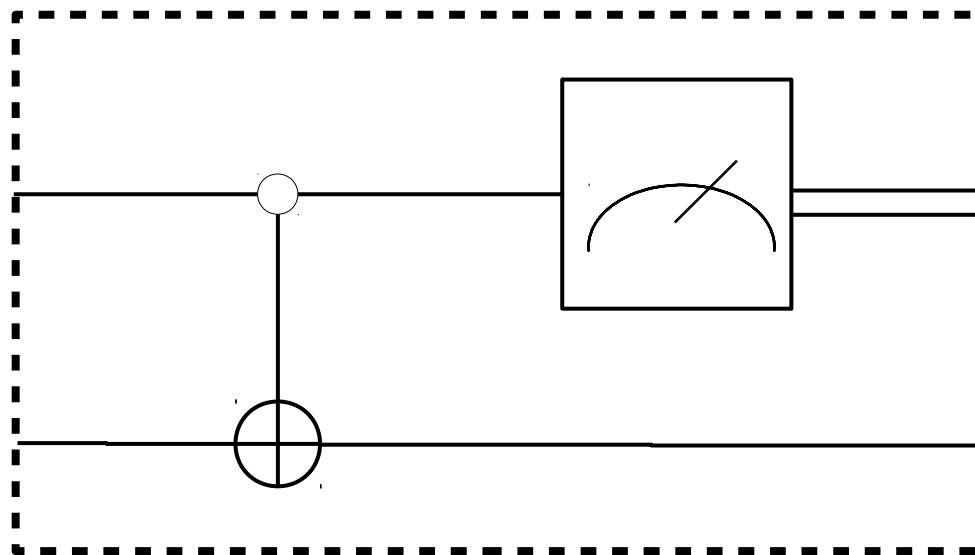


Boxes


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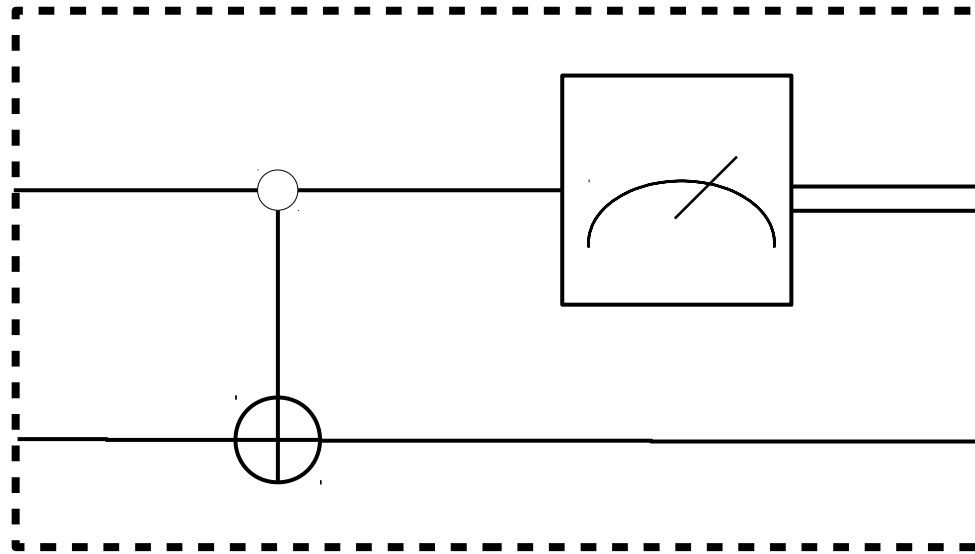
$$\Gamma \vdash \text{box}(q, r) \Rightarrow C$$
$$: \text{Circ}(\text{qubit} \otimes \text{qubit}, \text{bit} \otimes \text{qubit})$$

Boxes

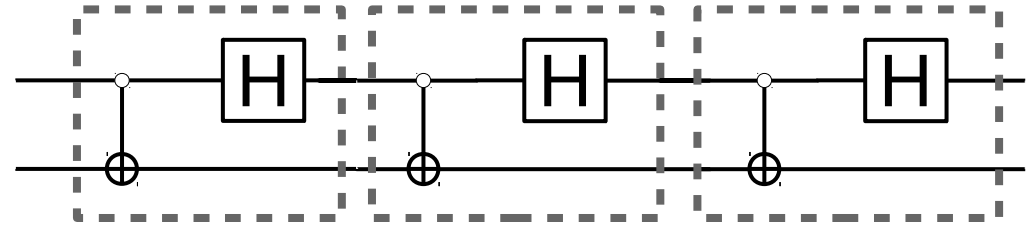

$$\Gamma; q : \underline{\text{qubit}}, r : \underline{\text{qubit}} \vdash C : \text{bit} \otimes \text{qubit}$$

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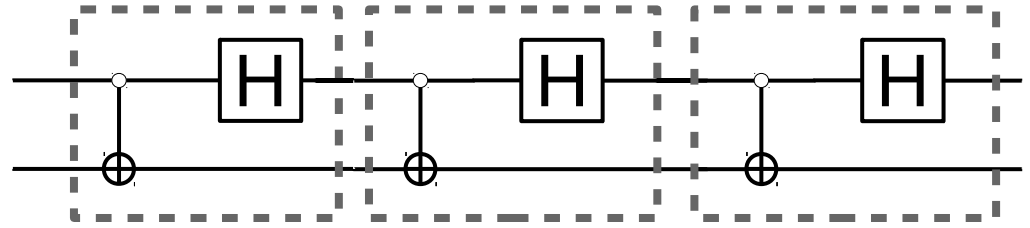
Boxes


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$$: \underline{\text{Circ}}(\underline{\text{qubit}} \otimes \text{qubit}, \underline{\text{bit}} \otimes \text{qubit})$$

n-ary Composition



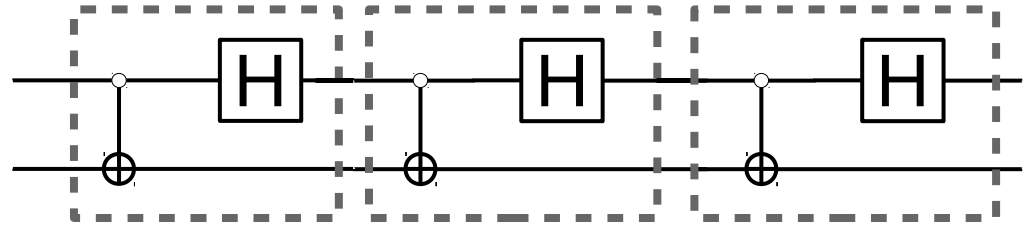
n-ary Composition



```
inSeqN (n : Nat) (c : Circ (W, W) )
      : Circ (W, W) =
  case n of
```



n-ary Composition



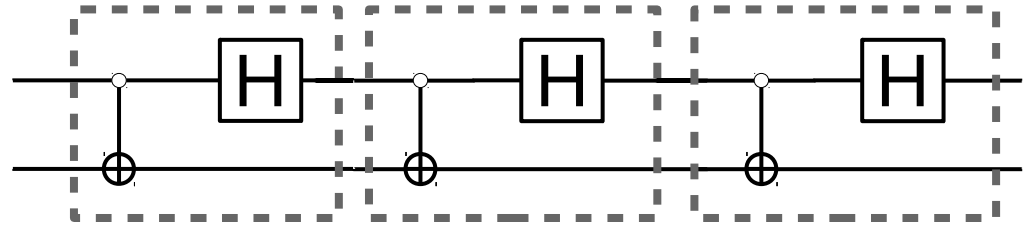
```
inSeqN (n : Nat) (c : Circ (W, W))
      : Circ (W, W) =
```

```
  case n of
```

```
    | 0 => box w => output w
```



n-ary Composition



```
inSeqN (n : Nat) (c : Circ (W, W))
      : Circ (W, W) =
```

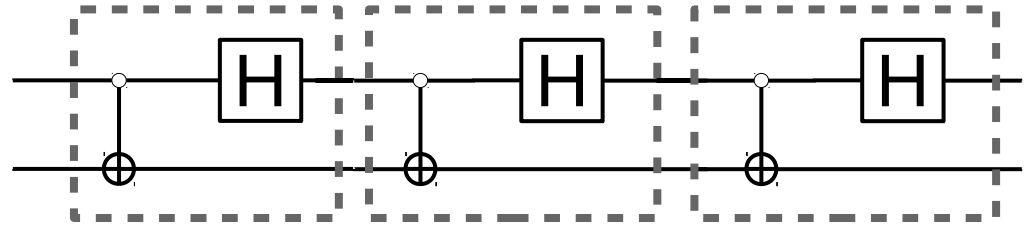
```
  case n of
```

```
    | 0    => box w => output w
```

```
    | m+1 => box w =>
```



n-ary Composition



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inSeqN (n : Nat) (c : Circ (W, W))
      : Circ (W, W) =
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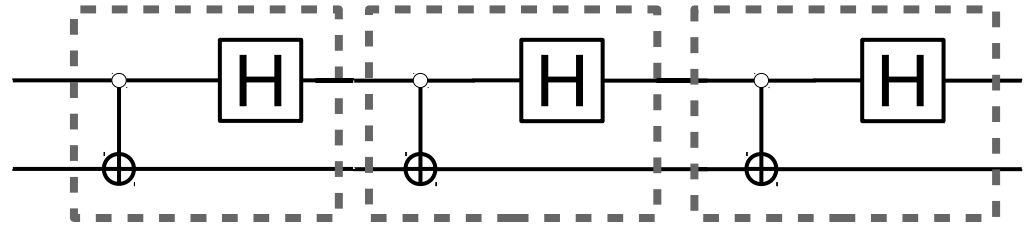
```
  case n of
```

```
    | 0    => box w => output w
```

```
    | m+1 => box w =>
              w' <- unbox c w;
```



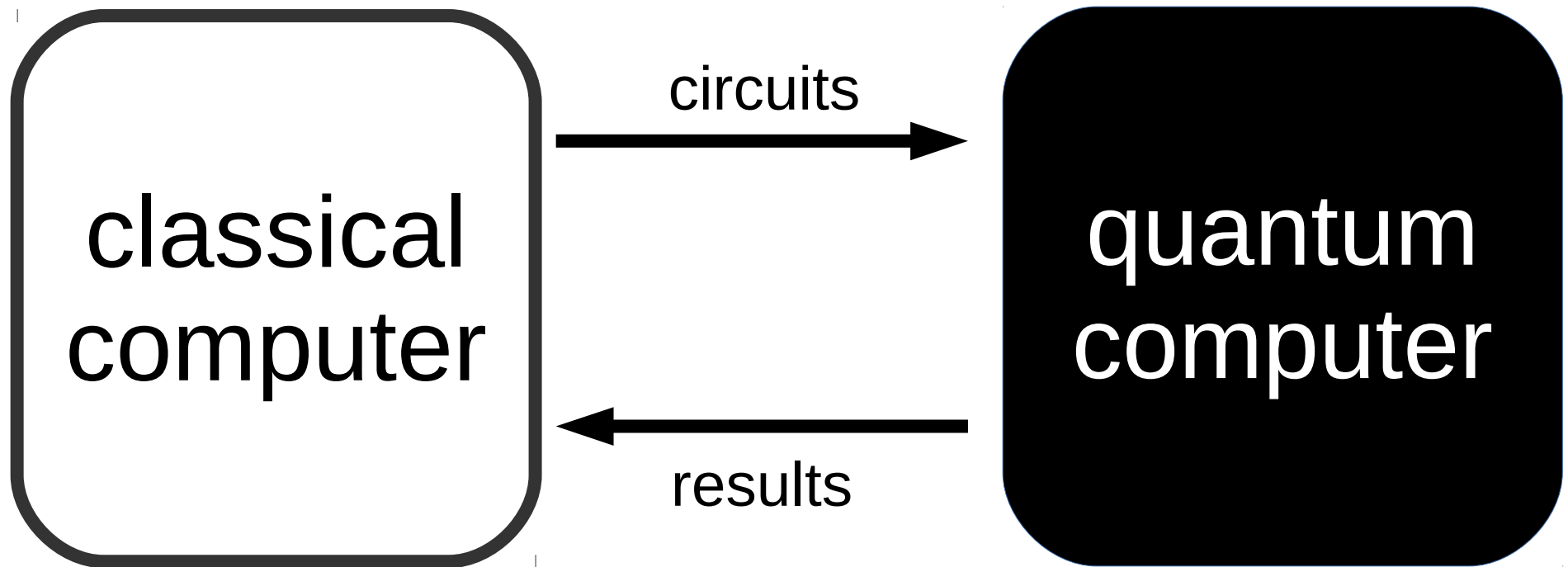
n-ary Composition



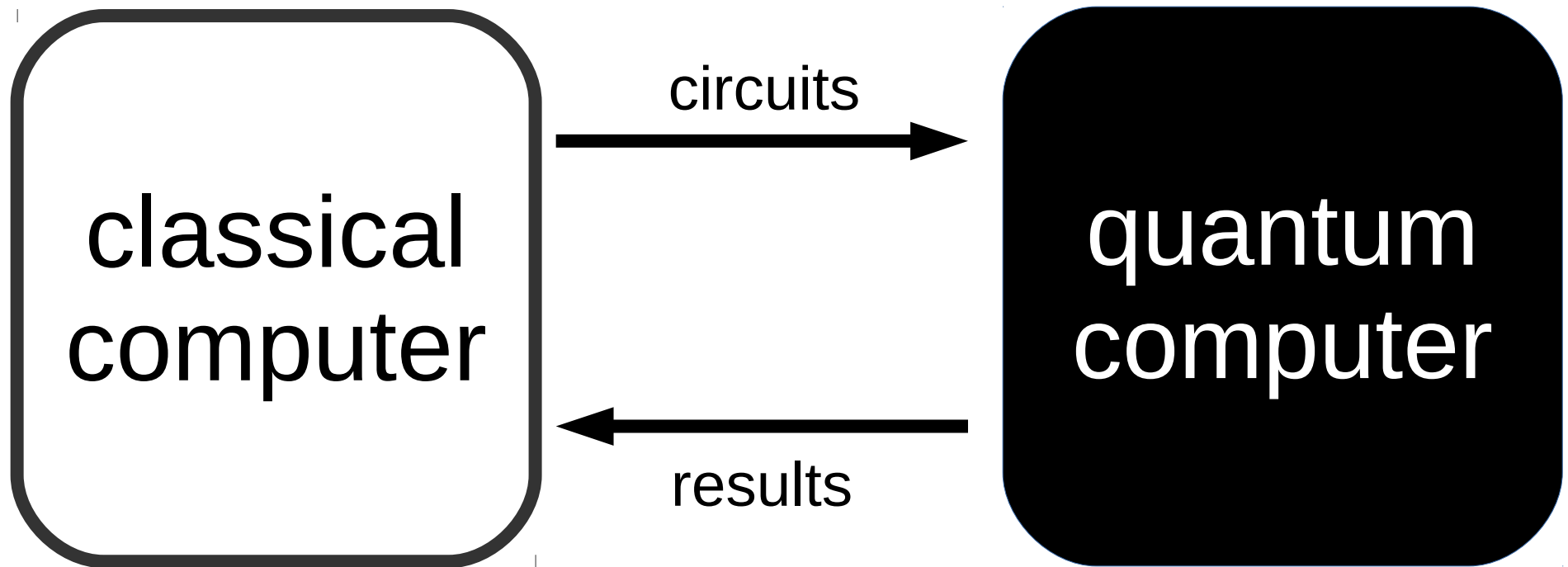
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inSeqN (n : Nat) (c : Circ (W, W))
      : Circ (W, W) =
  case n of
  | 0    => box w => output w
  | m+1 => box w =>
      w' <- unbox c w;
      unbox (inSeqN m) w'
```



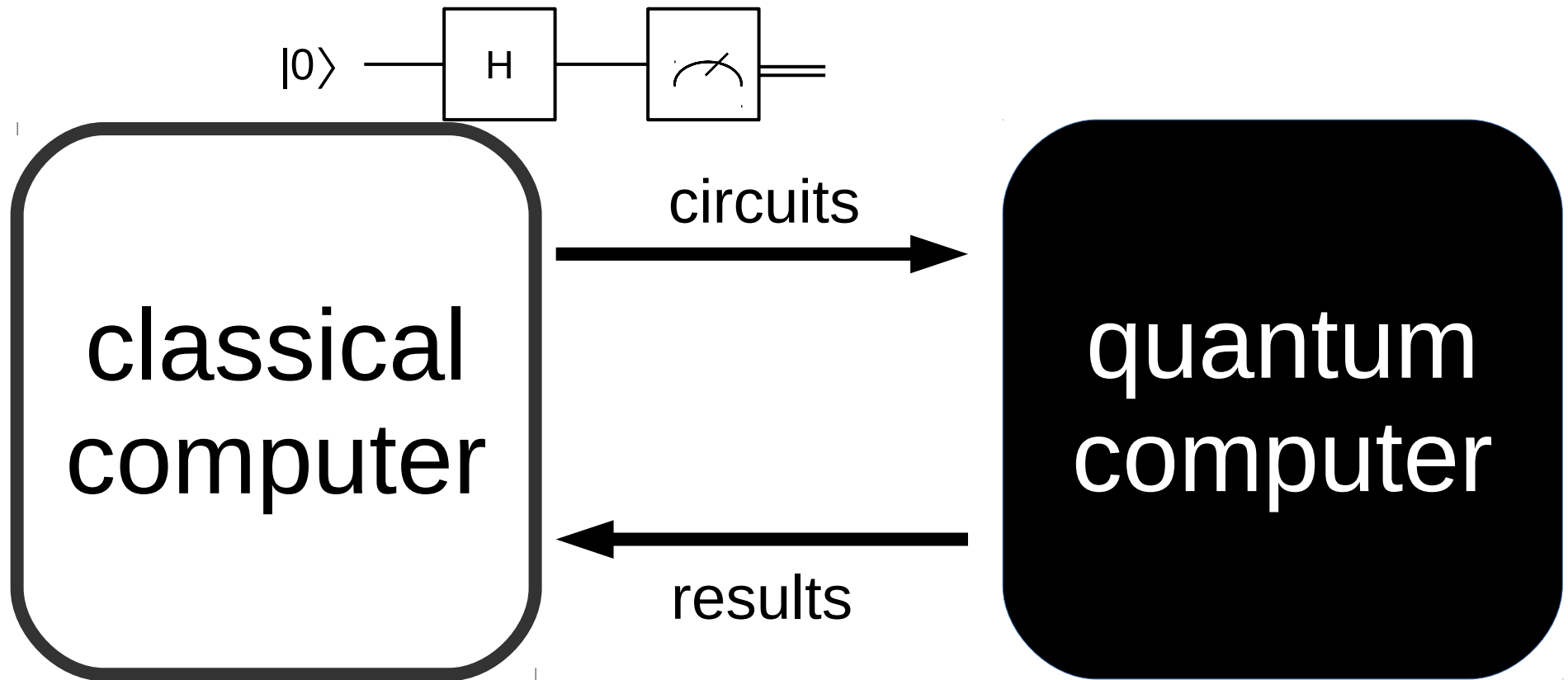
Communication



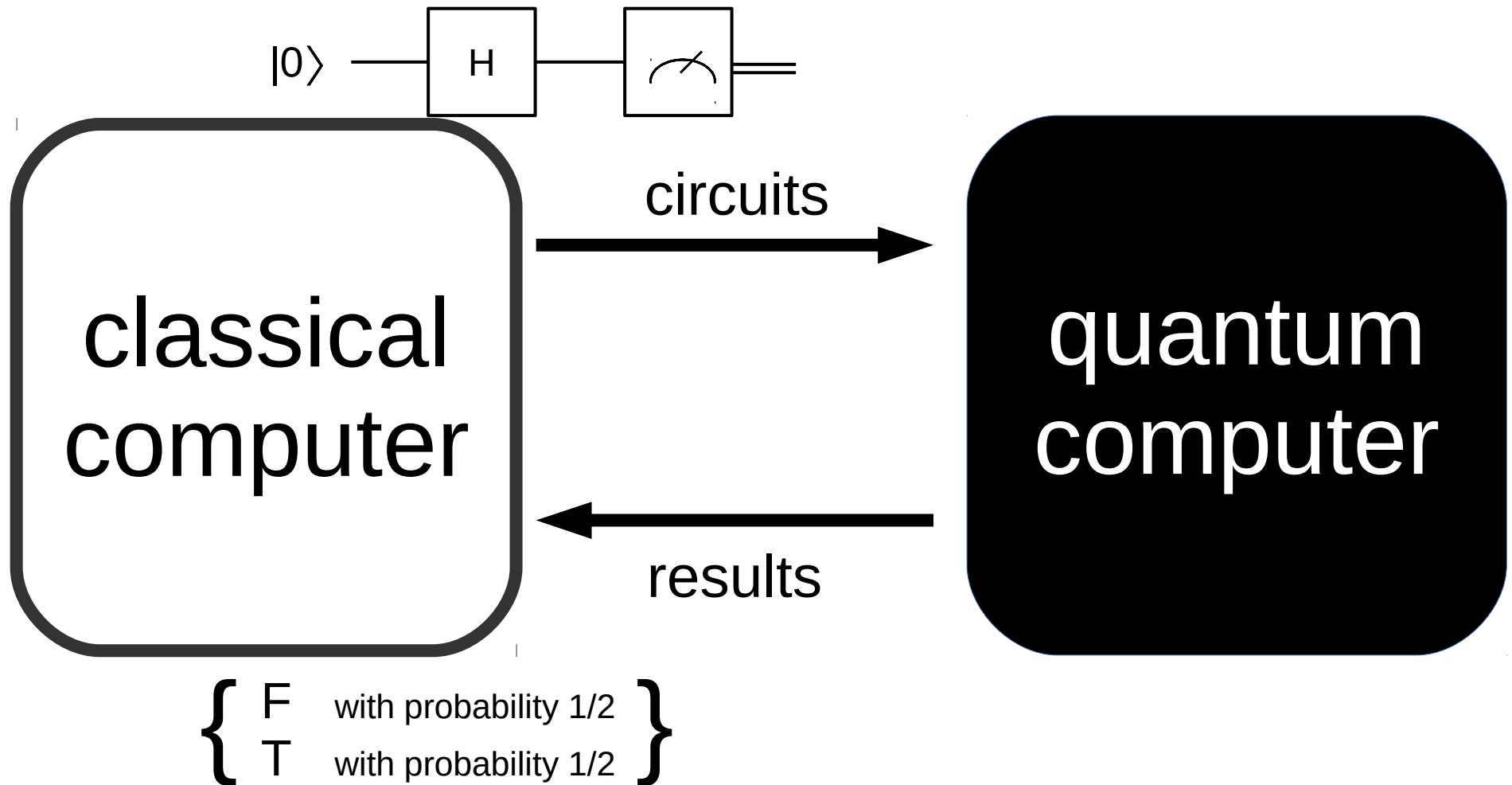
Running Circuits



Running Circuits



Running Circuits



Running Circuits

$$\frac{\Gamma; \cdot \vdash C : \text{bit}}{\Gamma \vdash \text{run } C : \text{Bool}}$$

$$\text{run}(|0\rangle \text{---} \boxed{\text{H}} \text{---} \boxed{\text{---}} \text{---}) = \left\{ \begin{array}{l} \text{F} \text{ with probability } 1/2 \\ \text{T} \text{ with probability } 1/2 \end{array} \right\}$$



Dynamic Lifting



Dynamic Lifting

`b ← gate meas q`



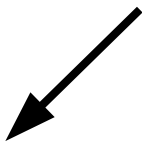
Dynamic Lifting

```
b ← gate meas q  
if b then ...  
    else ...
```



Dynamic Lifting

wire name



```
b ← gate meas q  
if b then ...  
    else ...
```



Dynamic Lifting

wire name

```
b ← gate meas q  
if b then ...  
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host language variable

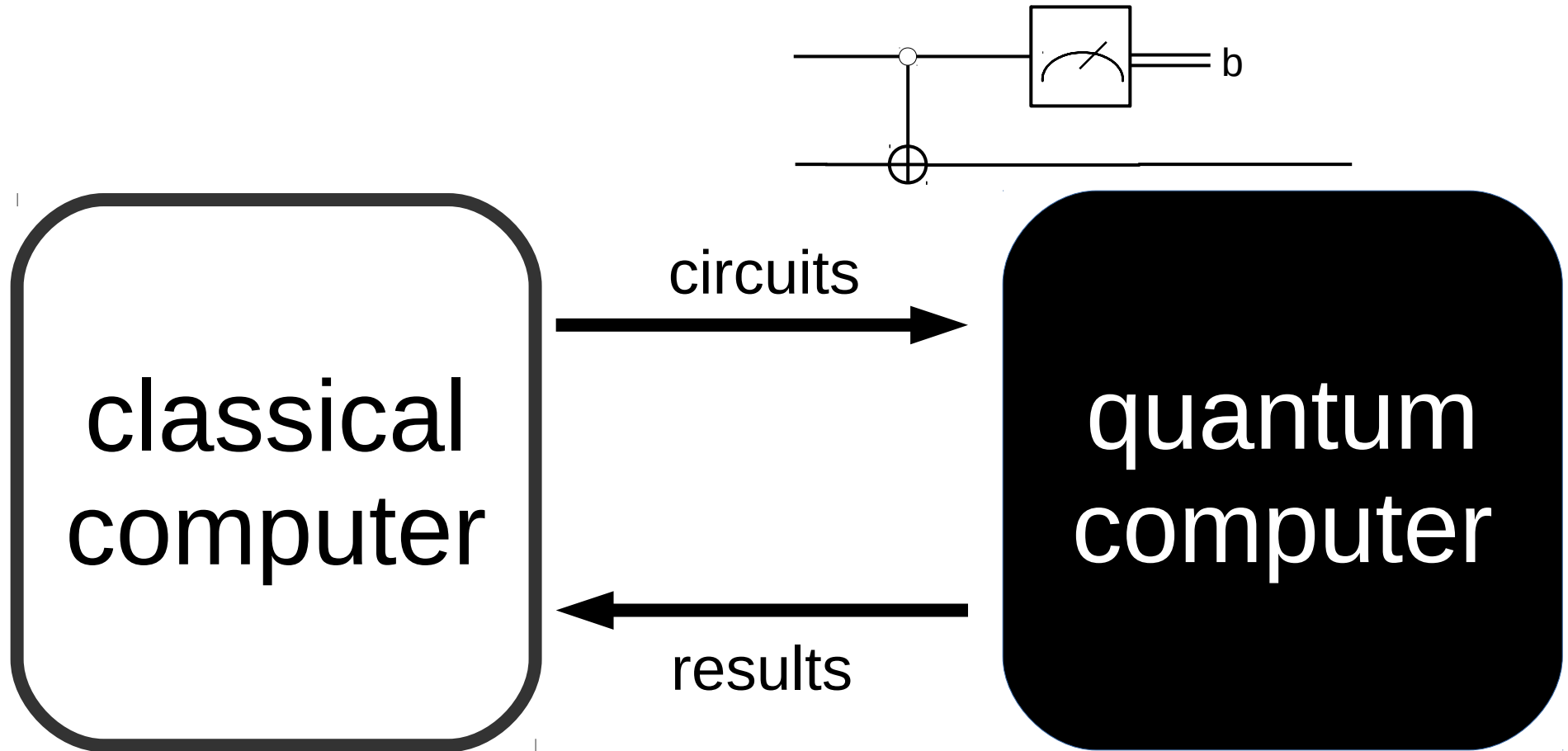


Dynamic Lifting

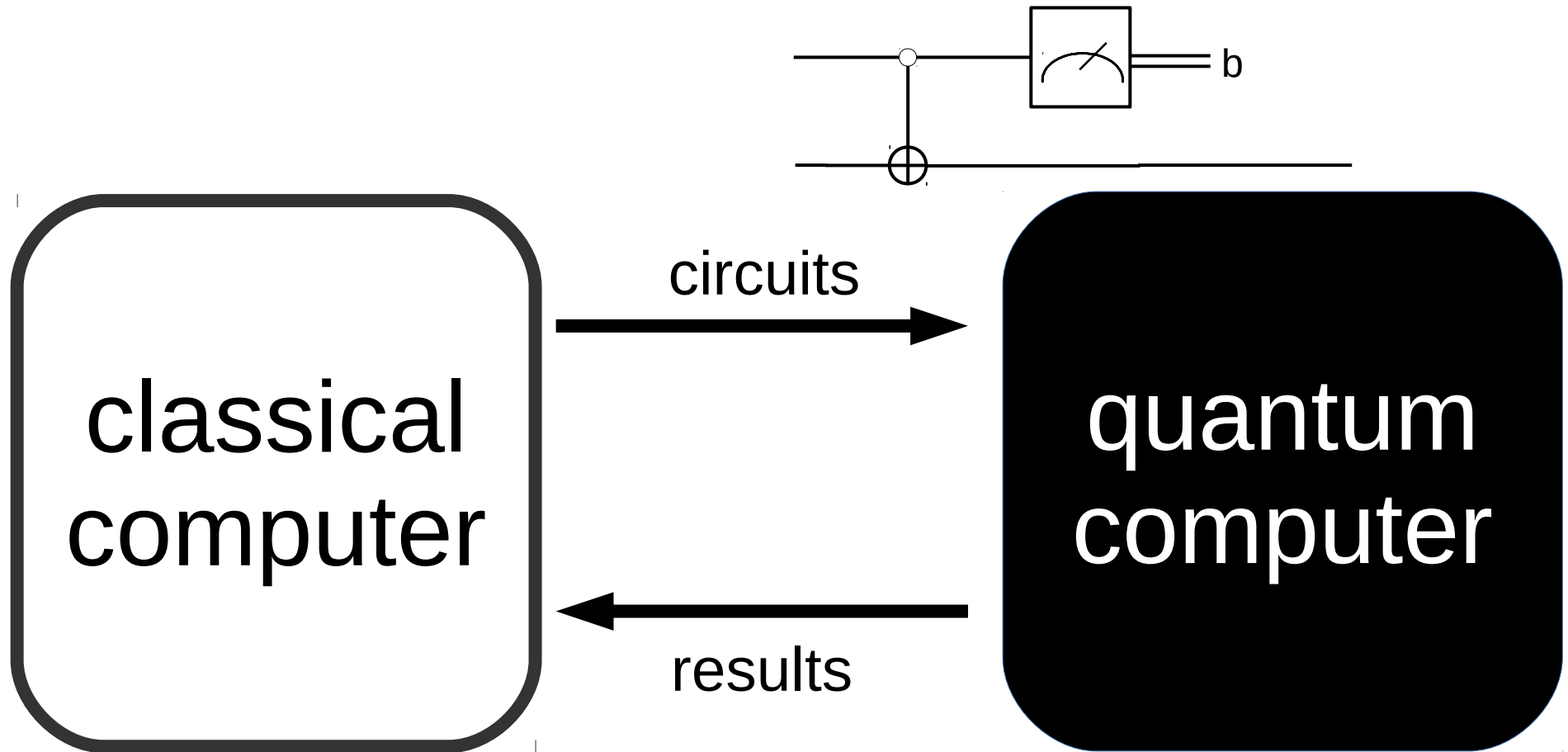
```
b ← gate meas q  
x ← lift b;  
unbox (if x then ...  
      else ...) q'
```



Dynamic Lifting



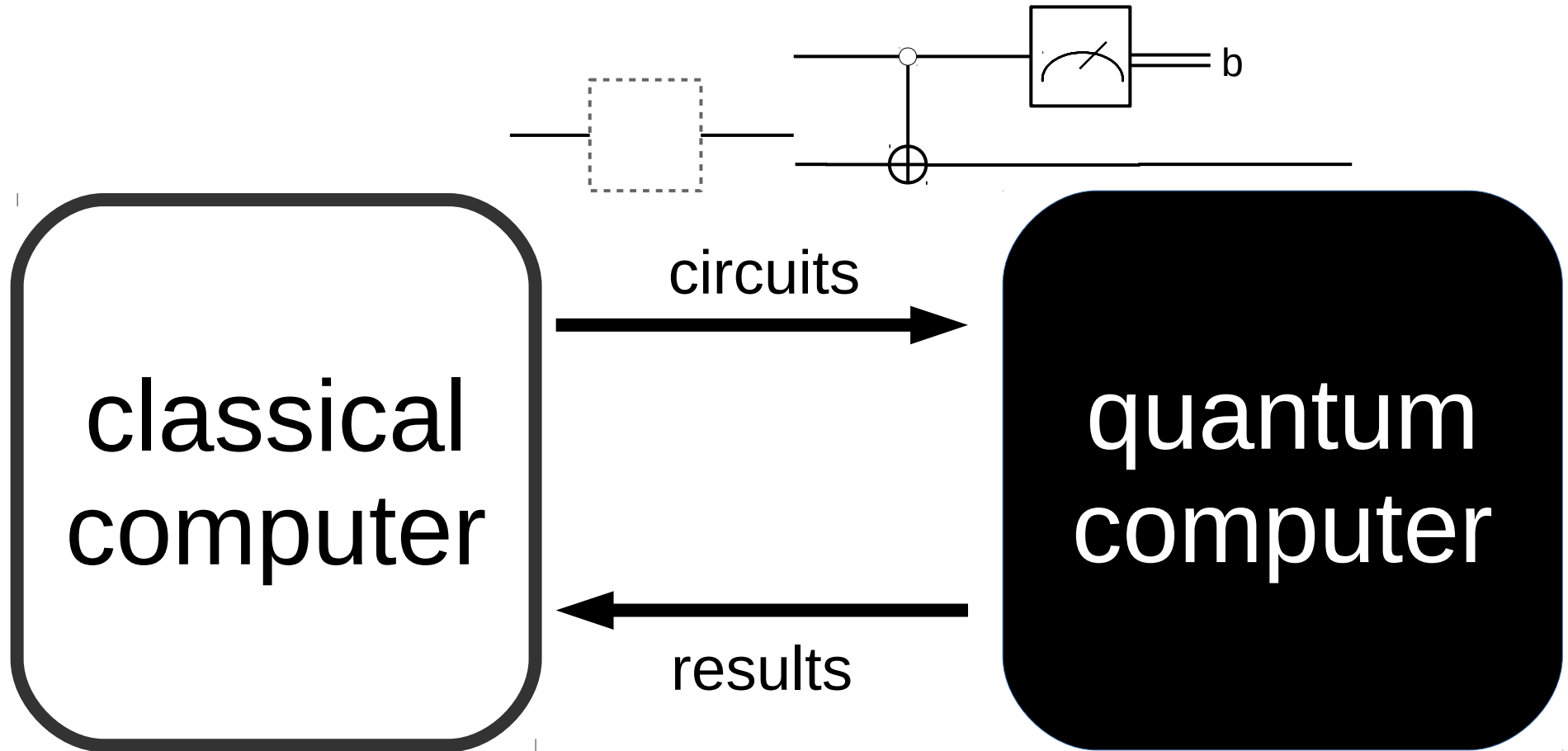
Dynamic Lifting



$\left\{ \begin{array}{l} 0 \text{ with probability } 1/2 \\ 1 \text{ with probability } 1/2 \end{array} \right\}$



Dynamic Lifting



$\left\{ \begin{array}{l} 0 \text{ with probability } 1/2 \\ 1 \text{ with probability } 1/2 \end{array} \right\}$



Summary

```
C ::= output p
    | p' ← gate g p; C
    | p ← C; C'
    | unbox t p
    | x ← lift p; C
```

```
t ::= ...
    | box p ⇒ C
    | run C
```



Summary

```
C ::= output p
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C ::= output p
    | p' ← gate g p; C
    | p ← C; C'
    | unbox t p
    | x ← lift p; C
```

```
t ::= ...
    | box p ⇒ C
    | run C
```



In the paper



In the paper

- operational semantics of circuits



In the paper

- **operational semantics of circuits**
 - proof of type safety
 - proof of strong normalization



In the paper

- **operational semantics of circuits**
 - proof of type safety
 - proof of strong normalization
- **denotational semantics of circuits**



quantum computations

=

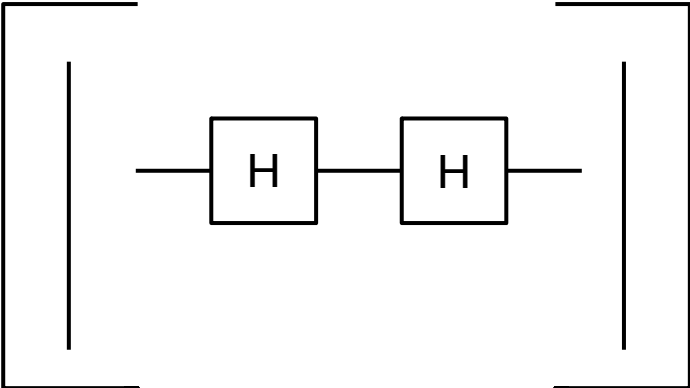
superoperators over density matrices



quantum computations

=

superoperators over density matrices


$$\left(\begin{array}{cc} \alpha & \beta \\ \gamma & \delta \end{array} \right) = \left(\begin{array}{cc} \alpha & \beta \\ \gamma & \delta \end{array} \right)$$



In the paper



In the paper

- **operational semantics of circuits**
 - proof of type safety
 - proof of strong normalization
- **denotational semantics of circuits**



In the paper

- **operational semantics of circuits**
 - proof of type safety
 - proof of strong normalization
- **denotational semantics of circuits**
 - proof of soundness



In the paper

- **operational semantics of circuits**
 - proof of type safety
 - proof of strong normalization
- **denotational semantics of circuits**
 - proof of soundness
- **dependently-typed circuits**



Dependent types

```
qubits (n : Nat) =  
  case n of  
  | 0      => 1  
  | m+1   => qubit ⊗ (qubits m)
```



Dependent types

```
qubits (n : Nat) =  
  case n of  
  | 0      => 1  
  | m+1   => qubit ⊗ (qubits m)
```

```
fourier : ∀ (n : nat).  
  Circ (qubits n, qubits n)
```

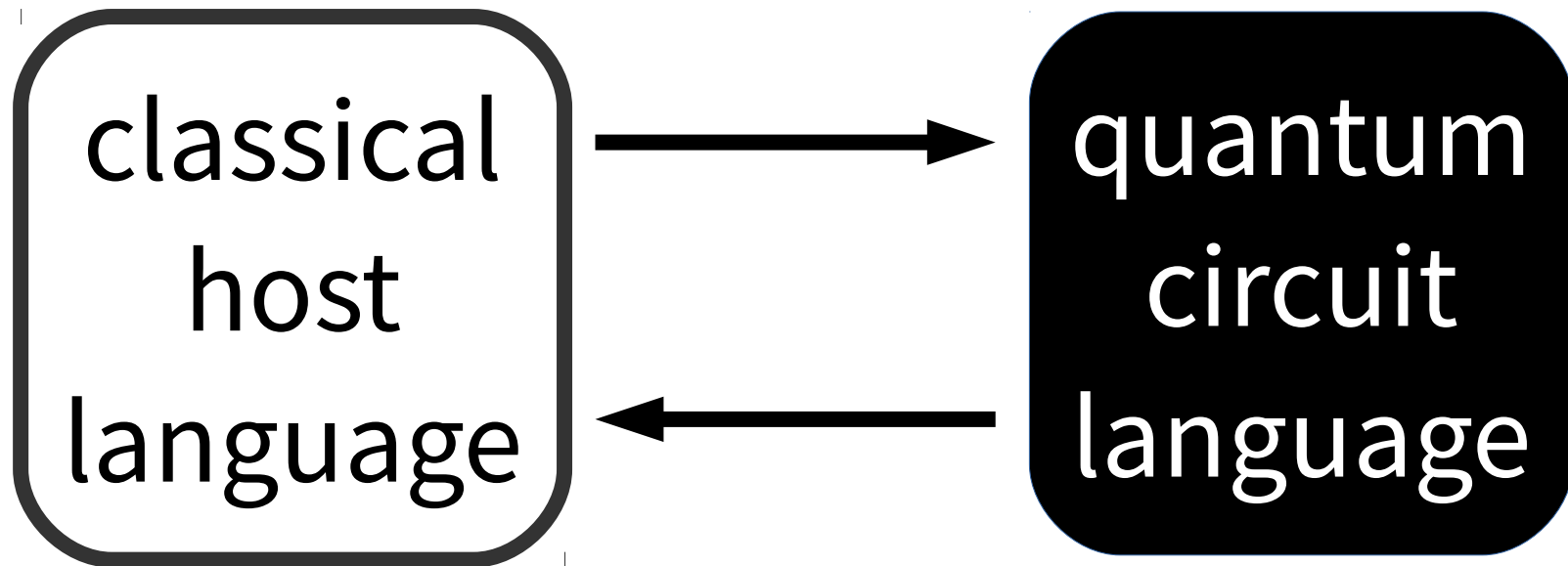


In the paper

- **operational semantics of circuits**
 - proof of type safety
 - proof of strong normalization
- **denotational semantics of circuits**
 - proof of soundness
- **dependently-typed circuits**
- **...and more!**



Thank you! Questions?



References

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Normal Forms

```
C ::= output p
    | p' ← gate g p; C
    | p ← C; C'
    | unbox t p
    | x ← lift p; C
```



Normal Forms

```
C ::= output p
    | p' ← gate g p; C
    | p ← C; C'
    | unbox t p
    | x ← lift p; C
```

```
N ::= output p
    | p' ← gate g p; N
    | x ← lift p; C
```



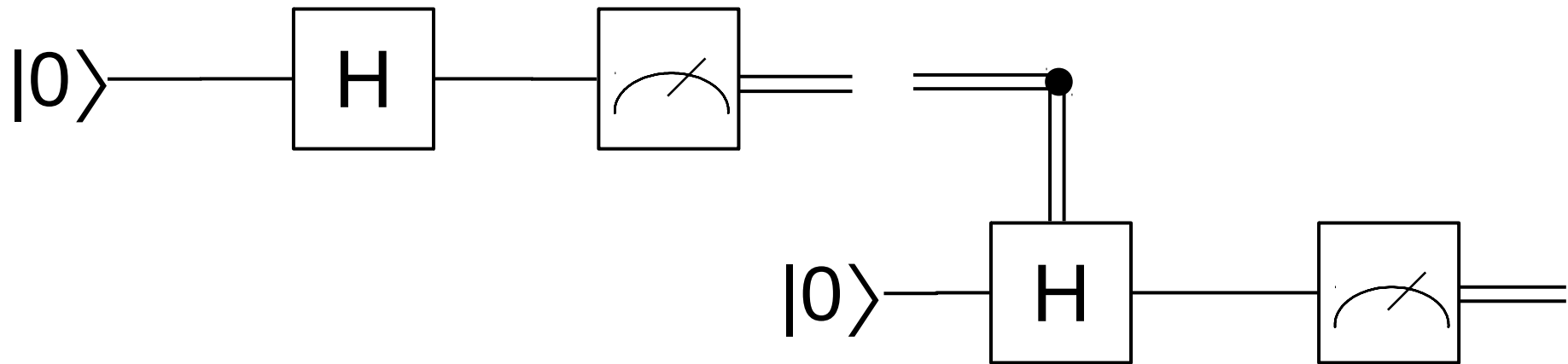
Types

$$t ::= \dots \mid \text{Circ}(W_1, W_2)$$


Types

$$W ::= \text{qubit} \mid \text{bit} \mid 1 \mid W_1 \otimes W_2$$
$$t ::= \dots \mid \text{Circ}(W_1, W_2)$$

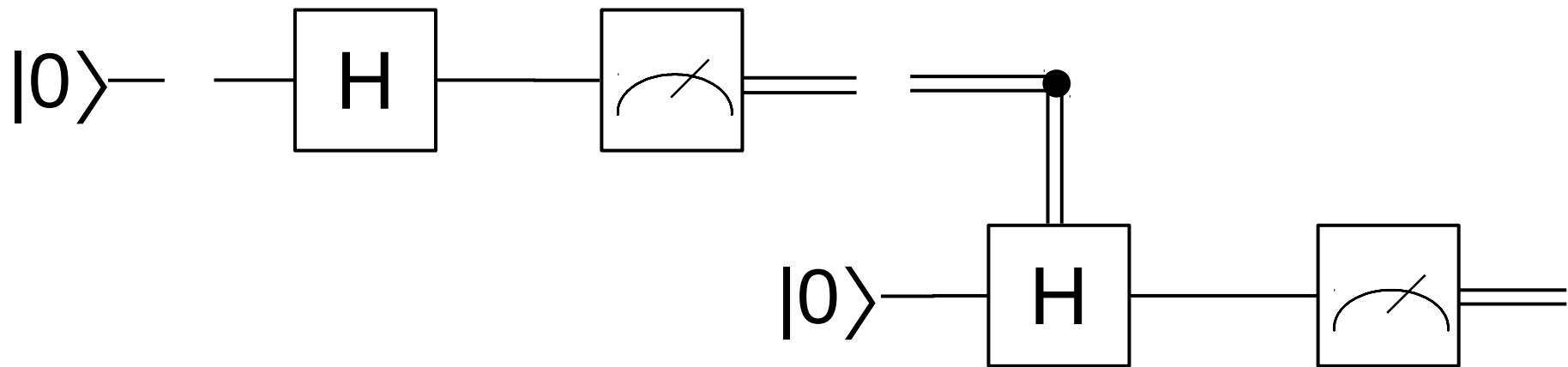

Composition is associative



```
b ← ( q ← gate new0 ();  
      q' ← gate H q ;  
      b' ← gate meas q' ;  
      output b' ) ;  
r ← new0 ();
```



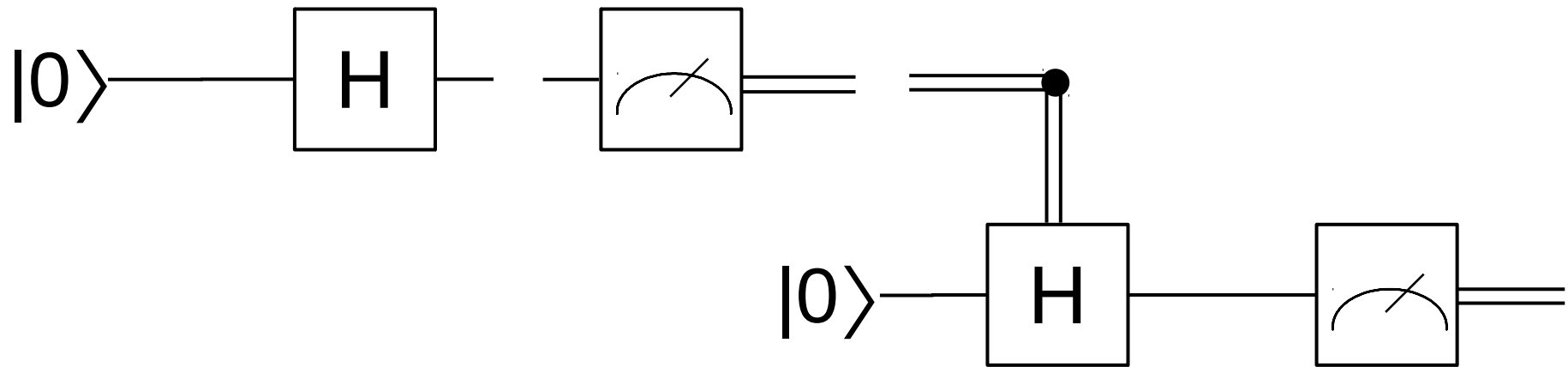
Composition is associative



```
q ← gate new0 ();  
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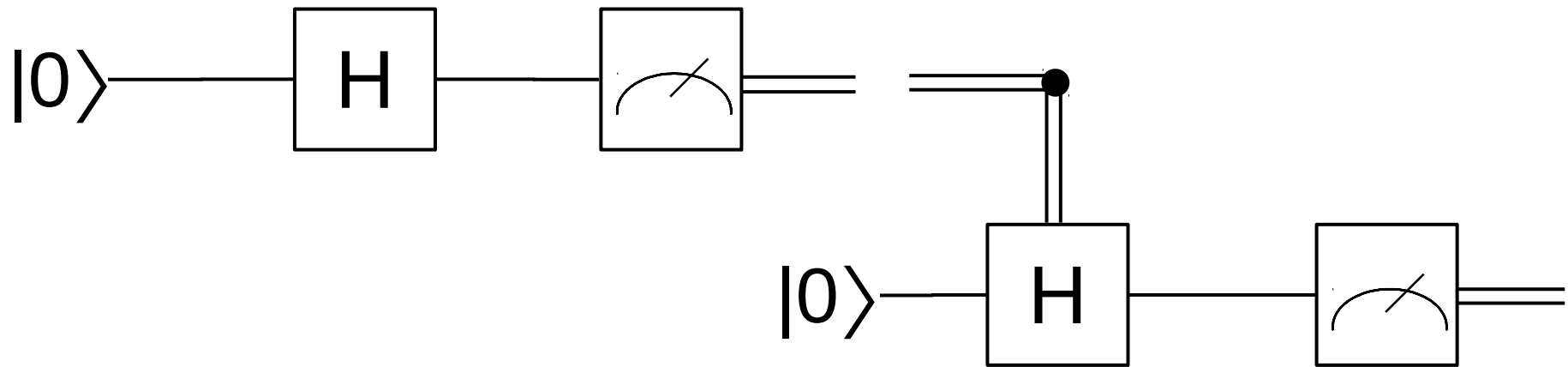
Composition is associative



```
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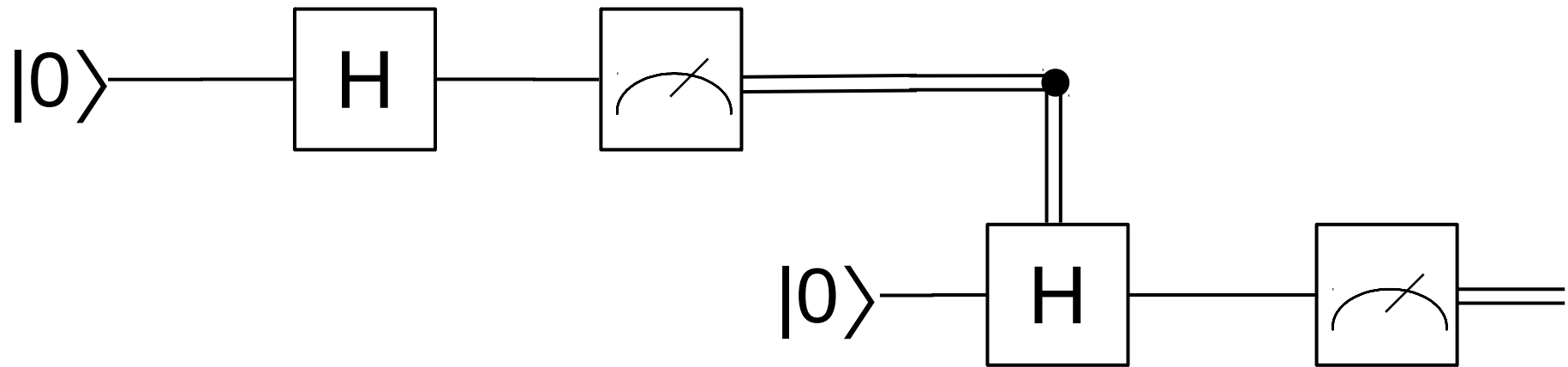
Composition is associative



```
q ← gate new0 ();  
q' ← gate H q ;  
b' ← gate meas q' ;  
b ← (output b' ) ;  
r ← new0 ();
```



Composition is associative



```
q ← gate new0 ();  
q' ← gate H q ;  
b' ← gate meas q' ;  
r ← new0 ();  
...
```

